

*Project:* DRA FRONT END FILTER PROJECT

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*Abstract:* This document contains the formal proofs relating to the value computations of DS/FMU/FEF/032; it forms part of the Phase II proofs for the DRA front end filter project RSRE 1C/6130.

*Distribution:* HAT FEF File  
Simon Wiseman

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## 0 DOCUMENT CONTROL

### 0.1 Contents List

<b>0</b>	<b>DOCUMENT CONTROL</b>	<b>2</b>
0.1	Contents List . . . . .	2
0.2	Document Cross References . . . . .	3
0.3	Changes History . . . . .	3
0.4	Changes Forecast . . . . .	3
<b>1</b>	<b>GENERAL</b>	<b>4</b>
1.1	Scope . . . . .	4
1.2	Introduction . . . . .	4
<b>2</b>	<b>PRELIMINARIES</b>	<b>4</b>
<b>3</b>	<b>MISCELLANY</b>	<b>4</b>
3.1	Consistency Proofs . . . . .	4
<b>4</b>	<b>LEMMAS</b>	<b>5</b>
<b>5</b>	<b>PROOF THAT <math>OK\_TC_d</math> GIVES <math>OkTableComputation</math></b>	<b>12</b>
<b>6</b>	<b>DATA OKNESS PROOFS</b>	<b>12</b>
6.1	Constant Expression . . . . .	12
6.2	<i>Contents</i> . . . . .	12
6.3	<i>Classification</i> . . . . .	13
6.4	<i>Countall</i> . . . . .	13
6.5	Monadic . . . . .	14
6.6	Binary . . . . .	14
6.7	Triadic . . . . .	21
6.8	Conversions . . . . .	21
6.9	Sterling . . . . .	21
6.10	Dinary . . . . .	21
6.11	Declarations . . . . .	21
6.12	Case Expressions . . . . .	22
6.13	Set Functions . . . . .	26
6.14	Count Functions . . . . .	31
6.15	<i>ExistsTuples</i> . . . . .	31
6.16	<i>SingleValue</i> . . . . .	32
6.17	<i>JoinedRowExistence</i> . . . . .	34
<b>7</b>	<b>CLASSIFICATION OKNESS PROOFS</b>	<b>34</b>
7.1	Constant Expression . . . . .	34
7.2	<i>Contents</i> . . . . .	35
7.3	<i>Classification</i> . . . . .	35
7.4	<i>Countall</i> . . . . .	36
7.5	Monadic . . . . .	36
7.6	Binary . . . . .	37

---

7.7	Triadic . . . . .	39
7.8	Conversions . . . . .	39
7.9	Sterling . . . . .	39
7.10	Dinary . . . . .	39
7.11	Declarations . . . . .	39
7.12	Case Expressions . . . . .	40
7.13	Set Functions . . . . .	44
7.14	Count Functions . . . . .	47
7.15	<i>ExistsTuples</i> . . . . .	48
7.16	<i>SingleValue</i> . . . . .	49
7.17	<i>JoinedRowExistence</i> . . . . .	50
<b>8</b>	<b>CLOSING DOWN</b>	<b>51</b>
<b>9</b>	<b>THE THEORY fef033</b>	<b>52</b>
9.1	Parents . . . . .	52
9.2	Children . . . . .	52
9.3	Theorems . . . . .	52
<b>10</b>	<b>INDEX</b>	<b>60</b>

## 0.2 Document Cross References

- [1] DS/FMU/FEF/018. *Proposal for Phase 2*. G.M. Prout, ICL Secure Systems, WIN01.
- [2] DS/FMU/FEF/026. *Critical Requirements on the SWORD Query Transformations*. R.D. Arthan, ICL Secure Systems, WIN01.
- [3] DS/FMU/FEF/031. *Execution Model Security Proofs*. R.D. Arthan, ICL Secure Systems, WIN01.
- [4] DS/FMU/FEF/032. *Table Computations for SWORD*. R.D. Arthan, ICL Secure Systems, WIN01.

## 0.3 Changes History

**Issue Revision : 1.16 (5 June 2016)** Unused lemmas removed; lemmas renamed.

**Issue 1.17** Removed dependency on ICL logo font

## 0.4 Changes Forecast

None.

# 1 GENERAL

## 1.1 Scope

This document provides a formal proof relating to the specifications in “An Execution Model for SWORD” [2]. It constitutes part of deliverable D13 of work package 3, as given in section 3 of the Proposal for Phase 2, [1].

(The current version is a rough draft of proofs of some of the propositions produced as pilot work during the third stage of phase 2.)

## 1.2 Introduction

# 2 PRELIMINARIES

The following ProofPower instructions set up a new theory *fef033* to hold the theorems to be proved and set up a proof context in which to carry out the proofs. Some theorems are required from the theory *fef031* defined in [3] and so this is made a parent of the theory *fef033*.

SML

```
|open_theory "fef032";
|force_delete_theory "fef033" handle _ => ();
|new_theory "fef033";
|new_parent "fef031";
|set_pc "hol";
```

# 3 MISCELLANY

## 3.1 Consistency Proofs

SML

```
|push_consistency_goal  $\Gamma$  TableComputations  $\bar{\cdot}$ ;
|a(prove_ $\exists$ _tac);
|a(strip_tac THEN rewrite_tac[tac_proof(([],  $\Gamma \forall x \bullet \exists z y \bullet (y, z) = x$ ),
strip_tac THEN  $\exists$ _tac  $\Gamma$  Snd  $x$  THEN  $\exists$ _tac  $\Gamma$  Fst  $x$  THEN rewrite_tac[])]);
|save_consistency_thm  $\Gamma$  TableComputations  $\bar{\cdot}$  (pop_thm());
```

---

## 4 LEMMAS

SML

```

set_pc"hol";
set_goal([],  $\lceil \forall i1\ i2 \bullet \text{BoolItem } i1 = \text{BoolItem } i2 \Leftrightarrow i1 = i2 \rceil$ );
a(rewrite_tac(map get_spec [ $\lceil \text{BoolItem} \rceil$ ,  $\lceil \text{ValuedItemItem} \rceil$ ])
  THEN REPEAT  $\forall$ -tac THEN  $\Leftrightarrow$ -tac THEN_TRY asm_rewrite_tac[]);
a(LEMMA_T  $\lceil \text{VI\_val}(\text{MkValuedItem } \text{sterling } (\text{BoolVal } i1))$ 
  =  $\text{VI\_val}(\text{MkValuedItem } \text{sterling } (\text{BoolVal } i2)) \rceil$ 
  (accept_tac o rewrite_rule(map get_spec [ $\lceil \text{MkValuedItem} \rceil$ ,  $\lceil \text{BoolVal} \rceil$ ])
  THEN1 asm_rewrite_tac[]);
val BoolItem_OneOne_lemma = save_pop_thm"BoolItem_OneOne_lemma";

```

SML

```

set_goal([],  $\lceil \forall c\ r1\ r2 \bullet$ 
   $\text{HideDerTableRow } c\ r1 = \text{HideDerTableRow } c\ r2 \Rightarrow$ 
   $\text{Length } (\text{DTR\_cols } r1) = \text{Length } (\text{DTR\_cols } r2) \rceil$ );
a(rewrite_tac(MkDerTableRow_lemma :: map get_spec [ $\lceil \text{HideDerTableRow} \rceil$ ,  $\lceil \text{Let} \rceil$ ]));
a(REPEAT strip_tac);
a(LEMMA_T  $\lceil \text{Length}(\text{Map}(\lambda (c', i) \bullet \text{if } c \text{ dominates } c'$ 
  then  $(c', i)$ 
  else  $(c', \text{ValuedItemItem } (\text{MkValuedItem } \text{sterling } \text{dummyVal}))$ 
  ( $\text{DTR\_cols } r1$ ))
  =  $\text{Length}(\text{Map}(\lambda (c', i) \bullet \text{if } c \text{ dominates } c'$ 
  then  $(c', i)$ 
  else  $(c', \text{ValuedItemItem } (\text{MkValuedItem } \text{sterling } \text{dummyVal}))$ 
  ( $\text{DTR\_cols } r2$ )) \rceil
  (strip_asm_tac o once_rewrite_rule[length_map_thm])
  THEN1 asm_rewrite_tac[]);
val HideDerTableRow_Length_lemma = save_pop_thm"HideDerTableRow_Length_lemma";

```

SML

```

set_goal([],  $\ulcorner \forall c\ r\ i \bullet$ 
   $1 \leq i \wedge i \leq \text{Length } (DTR\_cols\ r) \Rightarrow$ 
   $Nth\ (DTR\_cols\ (\text{HideDerTableRow}\ c\ r))\ i =$ 
   $(Fst\ (Nth\ (DTR\_cols\ r)\ i),$ 
  if  $c\ \text{dominates}\ (Fst\ (Nth\ (DTR\_cols\ r)\ i))$ 
  then  $Snd\ (Nth\ (DTR\_cols\ r)\ i)$ 
  else  $\text{ValuedItemItem}\ (\text{MkValuedItem}\ \text{sterling}\ \text{dummyVal}) \urcorner$ );
a(REPEAT  $\forall\_tac$ );
a(rewrite_tac(map get_spec[ $\ulcorner \text{Let} \urcorner$ ,  $\ulcorner DTR\_cols \urcorner$ ,  $\ulcorner \text{HideDerTableRow} \urcorner$ ]));
a(LEMMA_T  $\ulcorner \exists cols \bullet DTR\_cols\ r = cols \urcorner$ 
  (REPEAT_TTCL STRIP_THM_THEN rewrite_thm_tac)
  THEN1 prove_ $\exists\_tac$ );
a(intro_ $\forall\_tac$ ( $\ulcorner i \urcorner$ ,  $\ulcorner i \urcorner$ ) THEN list_induction_tac[ $\ulcorner cols \urcorner$ ]);
(* *** Goal "1" *** *)
a(rewrite_tac(map get_spec[ $\ulcorner \text{Length} \urcorner$ ]));
a(REPEAT strip_tac THEN all_var_elim_asm_tac);

```

SML

```

(* *** Goal "2" *** *)
a(rewrite_tac(map get_spec[ $\ulcorner \text{Length} \urcorner$ ,  $\ulcorner Nth \urcorner$ ]));
a(REPEAT  $\forall\_tac$ );
a(cases_tac  $\ulcorner i = 1 \urcorner$  THEN asm_rewrite_tac(map get_spec[ $\ulcorner \text{Map} \urcorner$ ,  $\ulcorner Nth \urcorner$ ])
  THEN REPEAT strip_tac);
(* *** Goal "2.1" *** *)
a(CASES_T  $\ulcorner c\ \text{dominates}\ Fst\ x \urcorner$  rewrite_thm_tac);
(* *** Goal "2.2" *** *)
a(GET_ASM_T  $\ulcorner 1 \leq i \urcorner$  (strip_asm_tac o rewrite_rule[get_spec[ $\ulcorner \$ \leq \urcorner$ ]]));
a(LEMMA_T  $\ulcorner i = i' + 1 \urcorner$  rewrite_thm_tac THEN1
  (POP_ASM_T (rewrite_thm_tac o eq_sym_rule)));
a(lemma_tac  $\ulcorner 1 \leq i' \wedge i' \leq \text{Length}\ cols \urcorner$  THEN1 PC_T1 "lin_arith" asm_prove_tac[]);
a(all_asm_fc_tac[]);
val Nth_HideDerTableRow_lemma = save_pop_thm "Nth_HideDerTableRow_lemma";

```

SML

```

push_goal([],  $\ulcorner \forall c \bullet DTR\_row\ o\ \text{HideDerTableRow}\ c = DTR\_row \urcorner$ );
a(PC_T1 "hol2" REPEAT strip_tac);
a(rewrite_tac(
  map get_spec[ $\ulcorner \$ o: ((a \rightarrow c) \rightarrow ((b \rightarrow a) \rightarrow (b \rightarrow c))) \urcorner$ ,
   $\ulcorner \text{HideDerTableRow} \urcorner$ ,  $\ulcorner DTR\_row \urcorner$ ,  $\ulcorner \text{Let} \urcorner$ ]);
val DTR_row_o_HideDerTableRow_lemma =
  save_pop_thm "DTR_row_o_HideDerTableRow_lemma";

```

SML

```

set_goal([],  $\lceil \forall f a b c \bullet f(\text{if } a \text{ then } b \text{ else } c) = (\text{if } a \text{ then } f b \text{ else } f c) \rceil$ );
a(REPEAT strip_tac THEN cases_tac $\lceil a:\text{BOOL} \rceil$  THEN asm_rewrite_tac[]);
val fun_if_thm = save_pop_thm"fun_if_thm";

```

SML

```

push_goal([],  $\lceil \forall c rl \bullet$ 
   $rl \upharpoonright \{r \mid c \text{ dominates } DTR\_row\ r \wedge c \text{ dominates } DTR\_where\ r\} = []$ 
 $\Leftrightarrow$ 
  (Map (HideDerTableRow c) (rl  $\upharpoonright \{r \mid c \text{ dominates } DTR\_row\ r\}$ )
     $\upharpoonright \{r \mid c \text{ dominates } DTR\_where\ r\} = [] \rceil$ );
a(REPEAT  $\forall\_tac$ );
a(list_induction_tac  $\lceil rl \rceil$  THEN asm_rewrite_tac(
  map get_spec $\lceil \lceil Let \rceil, \lceil Map \rceil, \lceil DTR\_row \rceil, \lceil HideDerTableRow \rceil, \lceil \$ \upharpoonright \rceil \rceil$ ));
(* *** Goal "1" *** *)
a(strip_tac THEN cases_tac  $\lceil c \text{ dominates } DTR\_row\ x \rceil$  THEN asm_rewrite_tac(
  map get_spec $\lceil \lceil Let \rceil, \lceil Map \rceil, \lceil DTR\_row \rceil, \lceil HideDerTableRow \rceil, \lceil \$ \upharpoonright \rceil \rceil$ ));
a(cases_tac $\lceil c \text{ dominates } DTR\_where\ x \rceil$  THEN asm_rewrite_tac[]);
(* *** Goal "2" *** *)
a(strip_tac THEN cases_tac  $\lceil c \text{ dominates } DTR\_row\ x \rceil$  THEN asm_rewrite_tac(
  map get_spec $\lceil \lceil Let \rceil, \lceil Map \rceil, \lceil DTR\_row \rceil, \lceil HideDerTableRow \rceil, \lceil \$ \upharpoonright \rceil \rceil$ ));
a(cases_tac $\lceil c \text{ dominates } DTR\_where\ x \rceil$  THEN asm_rewrite_tac[]);
val  $\upharpoonright\_null\_map\_hide\_lemma$  = save_pop_thm" $\upharpoonright\_null\_map\_hide\_lemma$ ";

```

SML

```

push_goal([],  $\lceil \forall c rl1\ rl2 \bullet$ 
  Map (HideDerTableRow c) rl1 = Map(HideDerTableRow c) rl2
 $\Rightarrow$ 
  Map (HideDerTableRow c) (rl1  $\upharpoonright \{r \mid c \text{ dominates } DTR\_where\ r\}$ )
  =
  Map (HideDerTableRow c) (rl2  $\upharpoonright \{r \mid c \text{ dominates } DTR\_where\ r\} \rceil$ );
a(REPEAT_N 2 strip_tac);
a(list_induction_tac  $\lceil rl1 \rceil$  THEN asm_rewrite_tac(
  map_null_thm::map get_spec $\lceil \lceil Map \rceil, \lceil \$ \upharpoonright \rceil \rceil$ ) THEN REPEAT strip_tac);
(* *** Goal "1" *** *)
a(asm_rewrite_tac $\lceil get\_spec \lceil \$ \upharpoonright \rceil \rceil$ );

```

SML

```

(* *** Goal "2" *** *)
a(POP_ASM_T ante_tac THEN strip_asm_tac(∀_elim⌈rl2⌋list_cases_thm)
  THEN all_var_elim_asm_tac1 THEN asm_rewrite_tac[get_spec⌈Map⌋]);
a(rewrite_tac(MkDerTableRow_lemma::
  map get_spec[⌈HideDerTableRow⌋, ⌈Let⌋, ⌈Map⌋, ⌈$⌋])
  THEN REPEAT strip_tac);
a(all_asm_fc_tac[] THEN asm_rewrite_tac[]);
a(cases_tac⌈c dominates DTR_where x'⌋ THEN asm_rewrite_tac[get_spec⌈Map⌋]);
a(rewrite_tac(MkDerTableRow_lemma::
  map get_spec[⌈HideDerTableRow⌋, ⌈Let⌋, ⌈Map⌋, ⌈$⌋])
  THEN REPEAT strip_tac);
val map_hide_map_hide_↓_lemma = save_pop_thm"map_hide_map_hide_↓_lemma";

```

SML

```

set_goal([], ⌈∀l a b • l ⊢ (a ∩ b) = (l ⊢ a) ⊢ b⌋);
a(REPEAT strip_tac);
a(list_induction_tac ⌈l⌋ THEN asm_rewrite_tac(
  map get_spec[⌈$⌋]));
a(strip_tac THEN PC_T1 "sets_ext1" rewrite_tac[]);
a(cases_tac⌈x ∈ a⌋ THEN asm_rewrite_tac[get_spec⌈$⌋]);
val ↓_∩_lemma = save_pop_thm"↓_∩_lemma";

```

SML

```

push_goal([], ⌈∀xy list •
  Split [] = ([], []) ∧
  Split (Cons xy list) =
  (Cons (Fst xy) (Fst (Split list)), Cons (Snd xy) (Snd (Split list)))⌋);
a(rewrite_tac[get_spec⌈Split⌋] THEN REPEAT strip_tac);
a(lemma_tac ⌈∃x y • xy = (x, y)⌋ THEN1
  (∃_tac⌈Fst xy⌋ THEN ∃_tac⌈Snd xy⌋) THEN
  asm_rewrite_tac[get_spec⌈Split⌋]);
val split_thm = save_pop_thm"split_thm";

```

SML

```

push_goal([], ⌈∀list • Length list = 0 ⇔ list = []⌋);
a(REPEAT strip_tac THEN LIST [id_tac, asm_rewrite_tac[get_spec⌈Length⌋]]);
a(POP_ASM_T ante_tac THEN list_induction_tac⌈list⌋
  THEN rename_tac[] THEN asm_rewrite_tac[get_spec⌈Length⌋]);
val length_0_thm = save_pop_thm"length_0_thm";

```



SML

```

push_goal([],  $\lceil \forall list \bullet \text{Length } list = 1 \Leftrightarrow \exists x \bullet list = [x] \rceil$ );
a(REPEAT strip_tac THEN_LIST [id_tac, asm_rewrite_tac[get_spec $\lceil \text{Length} \rceil$ ]]);
a(POP_ASM_T ante_tac THEN list_induction_tac $\lceil list \rceil$ 
  THEN rename_tac[] THEN asm_rewrite_tac[get_spec $\lceil \text{Length} \rceil$ , length_0_thm]);
a(REPEAT strip_tac THEN prove_ $\exists$ _tac);
val length_1_thm = save_pop_thm"length_1_thm";

```

SML

```

push_goal([],  $\lceil \forall list \bullet$ 
  Fst(Split list) = Map Fst list  $\wedge$ 
  Snd(Split list) = Map Snd list  $\rceil$ );
a(strip_tac);
a(list_induction_tac  $\lceil list \rceil$  THEN
  asm_rewrite_tac(split_thm::map get_spec $\lceil \text{Map} \rceil$ ));
val fst_snd_split_thm = save_pop_thm"fst_snd_split_thm";

```

SML

```

push_goal([],  $\lceil \forall c \bullet \text{lubl } [] = \text{lattice\_bottom} \wedge \text{lubl } (\text{Cons } c \text{ cl}) = c \text{ lub } \text{lubl } cl \rceil$ );
a(REPEAT  $\forall$ _tac THEN rewrite_tac(map get_spec $\lceil \text{lubl} \rceil$ ,  $\lceil \text{Fold} \rceil$ ));
val lubl_lemma = save_pop_thm"lubl_lemma";

```

SML

```

push_goal([],  $\lceil \forall c \bullet c \text{ lub } \text{lattice\_bottom} = c \rceil$ );
a(REPEAT strip_tac THEN lemma_tac
   $\lceil c \text{ dominates } c \text{ lub } \text{lattice\_bottom} \wedge$ 
   $c \text{ lub } \text{lattice\_bottom} \text{ dominates } c \rceil$ 
  THEN_LIST [rewrite_tac[get_spec $\lceil \$lub \rceil$ ], all_fc_tac[get_spec $\lceil \$lub \rceil$ ]]);
a(lemma_tac
   $\lceil c \text{ dominates } c \wedge c \text{ dominates } \text{lattice\_bottom} \rceil$ 
  THEN_LIST [rewrite_tac[get_spec $\lceil \$lub \rceil$ ], all_fc_tac[get_spec $\lceil \$lub \rceil$ ]]);
val lub_lattice_bottom_thm = save_pop_thm"lub_lattice_bottom_thm";

```

SML

```

push_goal([],  $\ulcorner \forall cc\ cil \bullet$ 
  cc dominates lubl (Map Fst cil)
 $\Rightarrow$ 
  Map ( $\lambda (c, i) \bullet (c, (if\ cc\ dominates\ c\ then\ i\ else\ Arbitrary))$ ) cil
  = cil
 $\urcorner$ );
a(REPEAT  $\forall\_tac$ );
a(list_induction_tac  $\ulcorner cil \urcorner$ 
  THEN asm_rewrite_tac(dominates_lub_lemma::lubl_lemma :: map get_spec[ $\ulcorner Map \urcorner$ ]));
a(REPEAT strip_tac THEN asm_rewrite_tac[]);
val dominates_lubl_map_hide_lemma = save_pop_thm "dominates_lubl_map_hide_lemma";

```

The following primitive recursive reformulation of *CaseVal* could perhaps do the case analysis more economically. Nonetheless it serves its purpose.

SML

```

set_goal([],  $\ulcorner \forall cc\ tst\ cev\ cevs\ ev \bullet$ 
  CaseVal cc tst [] ev =
    ( $\lambda tl\ rl\ r \bullet$ 
      let (tc, ti) = tst tl rl r
      in let (ec, ei) = ev tl rl r
      in if cc dominates tc
         then (ec, ei)
         else (tc, ei))
 $\wedge$ 
  CaseVal cc tst (Cons cev cevs) ev =
    ( $\lambda tl\ rl\ r \bullet$ 
      let (cr, ir) = CaseVal cc tst cevs ev tl rl r
      in let (tc, ti) = tst tl rl r
      in let (ce, cv) = cev
      in let (cec, cei) = ce tl rl r
      in let (cvc, cvi) = cv tl rl r
      in if ti = cei
         then if cc dominates tc  $\wedge$  cc dominates cec
            then (cvc, cvi)
            else if  $\neg$ cc dominates tc
            then (tc, cvi)
            else (cec, cvi)
         else if cc dominates tc  $\wedge$  cc dominates cec
            then (cr, ir)
            else if  $\neg$ cc dominates tc
            then (tc, ir)
            else (cec, ir))
 $\urcorner$ );

```

SML

```

a(PC_T1 "predicates1" rewrite_tac[]);
a(rewrite_tac(map get_spec [ $\ulcorner \$dom \urcorner$ ,  $\ulcorner CaseVal \urcorner$ ,  $\ulcorner CheckTest \urcorner$ ,
   $\ulcorner CaseValValue \urcorner$ ,  $\ulcorner CheckList \urcorner$ ,  $\ulcorner Let \urcorner$ ,  $\ulcorner Map \urcorner$ ]) THEN REPEAT strip_tac);
(* *** Goal "1" *** *)
a(cases_tac  $\ulcorner cc\ dominates\ Fst\ (tst\ x\ x'\ x'') \urcorner$  THEN asm_rewrite_tac[]);
(* *** Goal "2" *** *)
a(cases_tac  $\ulcorner cc\ dominates\ Fst\ (tst\ x\ x'\ x'') \urcorner$  THEN
  cases_tac  $\ulcorner cc\ dominates\ Fst\ (Fst\ cev\ x\ x'\ x'') \urcorner$  THEN
  cases_tac  $\ulcorner Snd\ (tst\ x\ x'\ x'') = Snd\ (Fst\ cev\ x\ x'\ x'') \urcorner$  THEN
  asm_rewrite_tac[]);
val CaseVal_lemma = save_pop_thm "CaseVal_lemma";

```

## 5 PROOF THAT $OK\_TC_d$ GIVES $OkTableComputation$

SML

```

|set_pc"hol";
|set_goal([],  $\ulcorner \forall c \bullet OK\_TC_d\ c \subseteq OkTableComputation\ c \urcorner$ );
|a(PC_T1"hol2"
|    rewrite_tac(map get_spec[ $\ulcorner OK\_TC_d \urcorner$ ,  $\ulcorner OkTableComputation \urcorner$ ,  $\ulcorner RiskInputs \urcorner$ ,  $\ulcorner Let \urcorner$ ])
|    THEN REPEAT strip_tac);
|a(DROP_NTH_ASM_T 2 (strip_asm_tac o eq_sym_rule));
|a(DROP_NTH_ASM_T 2 (strip_asm_tac o conv_rule(RAND_C eq_sym_conv)));
|a(all_asm_fc_tac[]);
|val OK_TC_d_lemma = save_pop_thm"OK_TC_d_lemma";

```

## 6 DATA OKNESS PROOFS

### 6.1 Constant Expression

SML

```

|set_goal([],  $\ulcorner \forall c\ ci \bullet DenoteConstant\ ci \in OK\_VC_d\ c \urcorner$ );
|a(rewrite_tac(map get_spec[ $\ulcorner OK\_VC_d \urcorner$ ,  $\ulcorner DenoteConstant \urcorner$ ]) THEN REPEAT strip_tac);
|val DenoteConstant_OK_d_lemma = save_pop_thm"DenoteConstant_OK_d_lemma";

```

### 6.2 Contents

SML

```

|set_goal([],  $\ulcorner \forall c\ i \bullet Contents\ i \in OK\_VC_d\ c \urcorner$ );
|a(rewrite_tac(map get_spec[ $\ulcorner OK\_VC_d \urcorner$ ,  $\ulcorner Contents \urcorner$ ])
|    THEN REPEAT strip_tac);
|a(POP_ASM_T ante_tac THEN all_fc_tac[HideDerTableRow_Length_lemma]);
|a(cases_tac  $\ulcorner 1 \leq i \wedge i \leq \#(DTR\_cols\ r_1) \urcorner$  THEN asm_rewrite_tac[]);
|a(lemma_tac  $\ulcorner i \leq \#(DTR\_cols\ r_0) \urcorner$  THEN1 asm_rewrite_tac[]);
|a(ALL_FC_T (MAP_EVERY(asm_tac o  $\forall\_elim \ulcorner c \urcorner$ )) [Nth_HideDerTableRow_lemma]);
|a(contr_tac);
|a(swap_nth_asm_concl_tac 4 THEN LIST_DROP_NTH_ASM_T [3, 8] rewrite_tac);
|a(cases_tac  $\ulcorner Fst\ (Nth\ (DTR\_cols\ r_1)\ i) = Fst\ (Nth\ (DTR\_cols\ r_0)\ i) \urcorner$ 
|    THEN asm_rewrite_tac[]);
|a(conv_tac (RAND_C eq_sym_conv) THEN asm_rewrite_tac[]);
|val Contents_OK_d_lemma = save_pop_thm"Contents_OK_d_lemma";

```

### 6.3 Classification

SML

```

set_goal([],  $\ulcorner \forall c \bullet \text{Classification } i \in \text{OK\_VC}_d \text{ c} \urcorner$ );
a(rewrite_tac(map get_spec[ $\ulcorner \text{OK\_VC}_d \urcorner$ ,  $\ulcorner \text{Classification} \urcorner$ ])
  THEN REPEAT strip_tac);
a(POP_ASM_T ante_tac THEN all_fc_tac[HideDerTableRow_Length_lemma]);
a(cases_tac  $\ulcorner 1 \leq i \wedge i \leq \# (\text{DTR\_cols } r_1) \urcorner$  THEN asm_rewrite_tac[]);
a(lemma_tac  $\ulcorner i \leq \# (\text{DTR\_cols } r_0) \urcorner$  THEN1 asm_rewrite_tac[]);
a(ALL_FC_T (MAP_EVERY(asm_tac o  $\forall$ _elim $\ulcorner c \urcorner$ )) [Nth_HideDerTableRow_lemma]);
a(contr_tac);
a(swap_nth_asm_concl_tac 4 THEN LIST_DROP_NTH_ASM_T [3, 8] rewrite_tac);
a(cases_tac  $\ulcorner \text{Fst } (\text{Nth } (\text{DTR\_cols } r_1) \ i) = \text{Fst } (\text{Nth } (\text{DTR\_cols } r_0) \ i) \urcorner$ 
  THEN asm_rewrite_tac[]);
a(DROP_NTH_ASM_T 3 ante_tac THEN asm_rewrite_tac[]);
val Classification_OKd_lemma = save_pop_thm "Classification_OKd_lemma";

```

### 6.4 Countall

SML

```

set_goal([],  $\ulcorner \forall c \bullet \text{CountAll} \in \text{OK\_VC}_d \text{ c} \urcorner$ );
a(rewrite_tac(map get_spec[ $\ulcorner \text{OK\_VC}_d \urcorner$ ,  $\ulcorner \text{CountAll} \urcorner$ ,  $\ulcorner \text{Let} \urcorner$ ]) THEN REPEAT strip_tac);
a(lemma_tac  $\ulcorner \neg (\# \text{rl}_0) = (\# \text{rl}_1) \urcorner$ 
  THEN1 PC_T1 "prop_eq" asm_prove_tac[]);
a(DROP_NTH_ASM_T 2 (fn x => id_tac));
a(POP_ASM_T ante_tac THEN POP_ASM_T ante_tac THEN POP_ASM_T ante_tac);
a(intro_ $\forall$ _tac( $\ulcorner r_1 \urcorner$ ,  $\ulcorner r_1 \urcorner$ ));
a(intro_ $\forall$ _tac( $\ulcorner r_0 \urcorner$ ,  $\ulcorner r_0 \urcorner$ ));
a(intro_ $\forall$ _tac( $\ulcorner \text{rl}_1 \urcorner$ ,  $\ulcorner \text{rl}_1 \urcorner$ ));
a(list_induction_tac  $\ulcorner \text{rl}_0 \urcorner$  THEN REPEAT strip_tac);
(* *** Goal "1" *** *)
a(strip_asm_tac ( $\forall$ _elim $\ulcorner \text{rl}_1 \urcorner$  list_cases_thm));
(* *** Goal "1.1" *** *)
a(DROP_NTH_ASM_T 2 ante_tac THEN asm_rewrite_tac[]);
(* *** Goal "1.2" *** *)
a(swap_asm_concl_tac
   $\ulcorner \text{Map } (\text{HideDerTableRow } c) \ [] = \text{Map } (\text{HideDerTableRow } c) \ \text{rl}_1 \urcorner$ 
  THEN asm_rewrite_tac(map get_spec[ $\ulcorner \text{Map} \urcorner$ ]));

```

SML

```

| (* *** Goal "2" *** *)
| a(strip_asm_tac (∀_elim⊢ rl1⊢ list_cases_thm));
| (* *** Goal "2.1" *** *)
| a(swap_asm_concl_tac
|   ⊢ Map (HideDerTableRow c) (Cons x rl0) = Map (HideDerTableRow c) rl1⊢
|   THEN asm_rewrite_tac(map_get_spec⊢ Map⊢));
| (* *** Goal "2.2" *** *)
| a(var_elim_nth_asm_tac 1);
| a(rewrite_tac[dominates_lub_lemma, lubl_lemma, get_spec⊢ Map⊢]);
| a(REPEAT strip_tac);
| a(DROP_NTH_ASM_T 4 (strip_asm_tac o rewrite_rule[get_spec⊢ Map⊢]));
| a(DROP_NTH_ASM_T 6 (ante_tac o list_∀_elim⊢ list2⊢, ⊢ x⊢, ⊢ x'⊢));
| a(REPEAT strip_tac);
| a(DROP_NTH_ASM_T 5 (strip_asm_tac o rewrite_rule[get_spec⊢ Length⊢]));
| val CountAll_OKd-lemma = save_pop_thm"CountAll_OKd-lemma";

```

## 6.5 Monadic

SML

```

| set_goal([], ⊢∀c f vc• vc ∈ OK_VCd c ⇒ MonOp f vc ∈ OK_VCd c⊢);
| a(rewrite_tac(map_get_spec⊢ OK_VCd⊢, ⊢ MonOp⊢, ⊢ Let⊢) THEN REPEAT strip_tac);
| a(lemma_tac⊢¬Snd (vc tl0 rl0 r0) = Snd (vc tl1 rl1 r1)⊢
|   THEN1 PC_T1 "prop_eq" asm_prove_tac[]);
| a(all_asm_fc_tac[]);
| val MonOp_OKd-lemma = save_pop_thm"MonOp_OKd-lemma";

```

## 6.6 Binary

SML

```

| set_goal([],
|   ⊢∀cc cil1 cil2•
|   Map (λ(c, i)• (c, if cc dominates c then i else Arbitrary)) cil1 =
|   Map (λ(c, i)• (c, if cc dominates c then i else Arbitrary)) cil2
|   ⇒ Fst(ComputeAnd cc cil1) = Fst(ComputeAnd cc cil2)
|   ∧ (cc dominates Fst(ComputeAnd cc cil1)
|     ⇒ Snd(ComputeAnd cc cil1) = Snd(ComputeAnd cc cil2))
|   ⊢);
| a(rewrite_tac(∀_elim⊢ Fst⊢ fun_if_thm :: ∀_elim⊢ Snd⊢ fun_if_thm ::
|   map_get_spec⊢ ComputeAnd⊢, ⊢ Map⊢, ⊢ Let⊢)
|   THEN REPEAT ∀_tac THEN strip_tac);
| a(lemma_tac⊢ cil1 ⊢ {(c, i)|cc dominates c ∧ ¬ ItemBool i}
|   = cil2 ⊢ {(c, i)|cc dominates c ∧ ¬ ItemBool i}⊢);

```

SML

```

(* *** Goal "1" *** *)
a(all_asm_ante_tac THEN intro_∀_tac(⌈ cil2 ⌋, ⌈ cil2 ⌋)
  THEN list_induction_tac ⌈ cil1 ⌋
  THEN asm_rewrite_tac(map get_spec[⌈ $ ⌋ ⌋, ⌈ Map ⌋]));
(* *** Goal "1.1" *** *)
a(rewrite_tac[map_null_thm]);
a(REPEAT strip_tac THEN asm_rewrite_tac[get_spec ⌈ $ ⌋ ⌋]);
(* *** Goal "1.2" *** *)
a(REPEAT strip_tac);
a(strip_asm_tac(∀_elim ⌈ cil2 ⌋ list_cases_thm) THEN all_var_elim_asm_tac1
  THEN POP_ASM_T ante_tac THEN rewrite_tac[get_spec ⌈ Map ⌋]);
a(REPEAT strip_tac THEN all_asm_fc_tac[]);
a(asm_rewrite_tac[get_spec ⌈ $ ⌋ ⌋]);
a(cases_tac ⌈ cc dominates Fst x' ⌋ THEN asm_rewrite_tac[]);
a(DROP_NTH_ASM_T 4 ante_tac THEN asm_rewrite_tac[]);
a(REPEAT strip_tac THEN asm_rewrite_tac[]);
a(cases_tac ⌈ ¬ ItemBool (Snd x') ⌋ THEN asm_rewrite_tac[]);
a(PC_T1 "prop_eq_pair" asm_prove_tac[]);

```

SML

```

(* *** Goal "2" *** *)
a(asm_rewrite_tac[]);
a(lemma_tac ⌈ Map Fst cil1 = Map Fst cil2 ⌋);
(* *** Goal "2.1" *** *)
a(LEMMA_T ⌈
  Map Fst(Map (λ (c, i) •
    (c, (if cc dominates c then i else Arbitrary)))) cil1) =
  Map Fst(Map (λ (c, i) •
    (c, (if cc dominates c then i else Arbitrary)))) cil2 ⌋
  (ante_tac o rewrite_rule[map_o_lemma])
  THEN1 asm_rewrite_tac[]);
a(LEMMA_T ⌈
  (Fst o (λ (c, i:Item) • (c, (if cc dominates c then i else Arbitrary))))
  = Fst ⌋ rewrite_thm_tac);
a(PC_T1 "hol2" rewrite_tac[]);

```

SML

```

(* *** Goal "2.2" *** *)
a(cases_tac⌈ cil2⌋ ⌈ {(c, i) | cc dominates c ∧ ¬ItemBool i} = []⌋ THEN
  asm_rewrite_tac[]);
a(strip_tac THEN LEMMA_T⌈ cil1 = cil2⌋ rewrite_thm_tac);
a(lemma_tac⌈ cc dominates lubl (Map Fst cil1)⌋ THEN1 asm_rewrite_tac[]);
a(all_fc_tac[dominates_lubl_map_hide_lemma]);
a(swap_nth_asm_concl_tac 8 THEN asm_rewrite_tac[]);
val ComputeAnd_lemma = save_pop_thm"ComputeAnd_lemma";

```

SML

```

set_goal([],
  ⌈∀c vcl • Elems vcl ⊆ OK_VC_d c ∧ Elems vcl ⊆ OK_VC_c c
  ⇒ BinOpAnd c vcl ∈ OK_VC_d c⌋);
a(rewrite_tac(
  map get_spec[⌈ BinOpAnd⌋, ⌈ OK_VC_d⌋, ⌈ OK_VC_c⌋])
  THEN REPEAT strip_tac THEN swap_nth_asm_concl_tac 1
  THEN POP_ASM_T ante_tac THEN strip_tac);
a(lemma_tac⌈
  Map (λ (c', i) • (c', (if c dominates c' then i else Arbitrary)))
  (Map (λ e • e tl0 rl0 r0) vcl) =
  Map (λ (c', i) • (c', (if c dominates c' then i else Arbitrary)))
  (Map (λ e • e tl1 rl1 r1) vcl)⌋
  THEN_LIST [id_tac, all_fc_tac[ComputeAnd_lemma]]);
a(POP_ASM_T discard_tac THEN
  asm_ante_tac⌈ Elems vcl ⊆ OK_VC_c c⌋ THEN
  asm_ante_tac⌈ Elems vcl ⊆ OK_VC_d c⌋
  THEN list_induction_tac⌈ vcl⌋);

```



SML

```

(* *** Goal "1" *** *)
a(asm_rewrite_tac[get_spec⌈Map⌋]);
(* *** Goal "2" *** *)
a(PC_T1"sets_ext1" asm_prove_tac[get_spec⌈Elms⌋]);
(* *** Goal "3" *** *)
a(PC_T1"sets_ext1" asm_prove_tac[get_spec⌈Elms⌋]);
(* *** Goal "4" *** *)
a(asm_rewrite_tac(map get_spec⌈Elms⌋, ⌈Map⌋) THEN REPEAT_N 3 strip_tac);
a(LEMMA_T⌈x ∈ OK_VCd c ∧ x ∈ OK_VCc c⌋
  (strip_asm_tac o rewrite_rule(map get_spec⌈OK_VCd⌋, ⌈OK_VCc⌋)) THEN1
  PC_T1"sets_ext1" asm_prove_tac[get_spec⌈Elms⌋]);
a(all_asm_fc_tac[] THEN asm_rewrite_tac[]);
a(cases_tac⌈c dominates Fst (x tl1 rl1 r1)⌋ THEN asm_rewrite_tac[]);
a(lemma_tac⌈c dominates Fst (x tl0 rl0 r0)⌋ THEN1 asm_rewrite_tac[]);
a(contr_tac THEN all_asm_fc_tac[]);
val BinOpAnd_OKd-lemma = save_pop_thm"BinOpAnd_OKd-lemma";

```

SML

```

set_goal([],
  ⌈∀cc cil1 cil2•
  Map (λ(c, i)• (c, if cc dominates c then i else Arbitrary)) cil1 =
  Map (λ(c, i)• (c, if cc dominates c then i else Arbitrary)) cil2
  ⇒ Fst(ComputeOr cc cil1) = Fst(ComputeOr cc cil2)
  ∧ (cc dominates Fst(ComputeOr cc cil1)
     ⇒ Snd(ComputeOr cc cil1) = Snd(ComputeOr cc cil2))
⌋);
a(rewrite_tac(∀_elim⌈Fst⌋ fun_if_thm :: ∀_elim⌈Snd⌋ fun_if_thm ::
  map get_spec⌈ComputeOr⌋, ⌈Map⌋, ⌈Let⌋)
  THEN REPEAT ∀_tac THEN strip_tac);
a(lemma_tac⌈cil1 ⊢ {(c, i)|cc dominates c ∧ ItemBool i}
  = cil2 ⊢ {(c, i)|cc dominates c ∧ ItemBool i}⌋);

```

SML

```

(* *** Goal "1" *** *)
a(all_asm_ante_tac THEN intro_∀_tac(⌈ cil2 ⌋, ⌈ cil2 ⌋)
  THEN list_induction_tac ⌈ cil1 ⌋
  THEN asm_rewrite_tac(map get_spec[⌈ $ ⌋ ⌋, ⌈ Map ⌋]));
(* *** Goal "1.1" *** *)
a(rewrite_tac[map_null_thm]);
a(REPEAT strip_tac THEN asm_rewrite_tac[get_spec ⌈ $ ⌋ ⌋]);
(* *** Goal "1.2" *** *)
a(REPEAT strip_tac);
a(strip_asm_tac(∀_elim ⌈ cil2 ⌋ list_cases_thm) THEN all_var_elim_asm_tac1
  THEN POP_ASM_T ante_tac THEN rewrite_tac[get_spec ⌈ Map ⌋]);
a(REPEAT strip_tac THEN all_asm_fc_tac[]);
a(asm_rewrite_tac[get_spec ⌈ $ ⌋ ⌋]);
a(cases_tac ⌈ cc dominates Fst x' ⌋ THEN asm_rewrite_tac[]);
a(DROP_NTH_ASM_T 4 ante_tac THEN asm_rewrite_tac[]);
a(REPEAT strip_tac THEN asm_rewrite_tac[]);
a(cases_tac ⌈ ¬ ItemBool (Snd x') ⌋ THEN asm_rewrite_tac[]);
a(PC_T1 "prop_eq_pair" asm_prove_tac[]);

```

SML

```

(* *** Goal "2" *** *)
a(asm_rewrite_tac[]);
a(lemma_tacΓ Map Fst cil1 = Map Fst cil2∇);
(* *** Goal "2.1" *** *)
a(LEMMA_TΓ
  Map Fst(Map (λ (c, i)•
    (c, (if cc dominates c then i else Arbitrary)))) cil1) =
  Map Fst(Map (λ (c, i)•
    (c, (if cc dominates c then i else Arbitrary)))) cil2∇
  (ante_tac o rewrite_rule[map_o_lemma])
  THEN1 asm_rewrite_tac[]);
a(LEMMA_TΓ
  (Fst o (λ (c, i:Item) • (c, (if cc dominates c then i else Arbitrary))))
  = Fst∇ rewrite_thm_tac);
a(PC_T1"hol2" rewrite_tac[]);
(* *** Goal "2.2" *** *)
a(cases_tacΓ cil2 ∩ {(c, i)|cc dominates c ∧ ItemBool i} = []∇ THEN
  asm_rewrite_tac[]);
a(strip_tac THEN LEMMA_TΓ cil1 = cil2∇ rewrite_thm_tac);
a(lemma_tacΓ cc dominates lubl (Map Fst cil1)∇ THEN1 asm_rewrite_tac[]);
a(all_fc_tac[dominates_lubl_map_hide_lemma]);
a(swap_nth_asm_concl_tac 8 THEN asm_rewrite_tac[]);
val ComputeOr_lemma = save_pop_thm"ComputeOr_lemma";

```

SML

```

set_goal([],
  ⌈∀c vcl • Elems vcl ⊆ OK_VC_d c ∧ Elems vcl ⊆ OK_VC_c c
  ⇒ BinOpOr c vcl ∈ OK_VC_d c⌋);
a(rewrite_tac(
  map get_spec[⌈BinOpOr⌋, ⌈OK_VC_d⌋, ⌈OK_VC_c⌋])
  THEN REPEAT strip_tac THEN swap_nth_asm_concl_tac 1
  THEN POP_ASM_T ante_tac THEN strip_tac);
a(lemma_tac⌈
  Map (λ (c', i) • (c', (if c dominates c' then i else Arbitrary)))
  (Map (λ e • e tl_0 rl_0 r_0) vcl) =
  Map (λ (c', i) • (c', (if c dominates c' then i else Arbitrary)))
  (Map (λ e • e tl_1 rl_1 r_1) vcl)⌋
  THEN_LIST [id_tac, all_fc_tac[ComputeOr_lemma]]);
a(POP_ASM_T discard_tac THEN
  asm_ante_tac ⌈Elems vcl ⊆ OK_VC_c c⌋ THEN
  asm_ante_tac ⌈Elems vcl ⊆ OK_VC_d c⌋
  THEN list_induction_tac⌈vcl⌋);

```

SML

```

(* *** Goal "1" *** *)
a(asm_rewrite_tac[get_spec⌈Map⌋]);
(* *** Goal "2" *** *)
a(PC_T1"sets_ext1" asm_prove_tac[get_spec⌈Elems⌋]);
(* *** Goal "3" *** *)
a(PC_T1"sets_ext1" asm_prove_tac[get_spec⌈Elems⌋]);
(* *** Goal "4" *** *)
a(asm_rewrite_tac(map get_spec[⌈Elems⌋, ⌈Map⌋]) THEN REPEAT_N 3 strip_tac);
a(LEMMA_T⌈x ∈ OK_VC_d c ∧ x ∈ OK_VC_c c⌋
  (strip_asm_tac o rewrite_rule(map get_spec[⌈OK_VC_d⌋, ⌈OK_VC_c⌋])) THEN1
  PC_T1"sets_ext1" asm_prove_tac[get_spec⌈Elems⌋]);
a(all_asm_fc_tac[] THEN asm_rewrite_tac[]);
a(cases_tac⌈c dominates Fst (x tl_1 rl_1 r_1)⌋ THEN1 asm_rewrite_tac[]);
a(lemma_tac⌈c dominates Fst (x tl_0 rl_0 r_0)⌋ THEN1 asm_rewrite_tac[]);
a(contr_tac THEN all_asm_fc_tac[]);
val BinOpOr_OK_d_lemma = save_pop_thm"BinOpOr_OK_d_lemma";

```

SML

```

set_goal([],  $\lceil \forall c f \text{ vc1 vc2} \bullet \text{vc1} \in \text{OK\_VC}_d \ c \wedge \text{vc2} \in \text{OK\_VC}_d \ c$ 
 $\Rightarrow \text{BinOp } f \ \text{vc1} \ \text{vc2} \in \text{OK\_VC}_d \ c \rceil$ );
a(rewrite_tac(dominates_lub_lemma :: map get_spec [ $\lceil \text{OK\_VC}_d \rceil$ ,  $\lceil \text{BinOp} \rceil$ ,  $\lceil \text{Let} \rceil$ ])
  THEN REPEAT strip_tac);
a(lemma_tac  $\lceil \text{Snd} \ (\text{vc2} \ \text{tl}_0 \ \text{rl}_0 \ r_0) = \text{Snd} \ (\text{vc2} \ \text{tl}_1 \ \text{rl}_1 \ r_1) \rceil$ 
  THEN1 (contr_tac THEN all_asm_fc_tac));
a(lemma_tac  $\lceil \neg \text{Snd} \ (\text{vc1} \ \text{tl}_0 \ \text{rl}_0 \ r_0) = \text{Snd} \ (\text{vc1} \ \text{tl}_1 \ \text{rl}_1 \ r_1) \rceil$ 
  THEN1 PC_T1 "prop_eq" asm_prove_tac);
a(all_asm_fc_tac);
val BinOp_OKd_lemma = save_pop_thm "BinOp_OKd_lemma";

```

## 6.7 Triadic

SML

```

set_goal([],  $\lceil \forall c f \text{ vc1 vc2 vc3} \bullet$ 
 $\text{vc1} \in \text{OK\_VC}_d \ c \wedge \text{vc2} \in \text{OK\_VC}_d \ c \wedge \text{vc3} \in \text{OK\_VC}_d \ c \Rightarrow$ 
 $\text{TriOp } f \ \text{vc1} \ \text{vc2} \ \text{vc3} \in \text{OK\_VC}_d \ c \rceil$ );
a(rewrite_tac(dominates_lub_lemma :: map get_spec [ $\lceil \text{OK\_VC}_d \rceil$ ,  $\lceil \text{TriOp} \rceil$ ,  $\lceil \text{Let} \rceil$ ])
  THEN REPEAT strip_tac);
a(lemma_tac  $\lceil \text{Snd} \ (\text{vc2} \ \text{tl}_0 \ \text{rl}_0 \ r_0) = \text{Snd} \ (\text{vc2} \ \text{tl}_1 \ \text{rl}_1 \ r_1) \wedge$ 
 $\text{Snd} \ (\text{vc3} \ \text{tl}_0 \ \text{rl}_0 \ r_0) = \text{Snd} \ (\text{vc3} \ \text{tl}_1 \ \text{rl}_1 \ r_1) \rceil$ 
  THEN1 (contr_tac THEN all_asm_fc_tac));
a(lemma_tac  $\lceil \neg \text{Snd} \ (\text{vc1} \ \text{tl}_0 \ \text{rl}_0 \ r_0) = \text{Snd} \ (\text{vc1} \ \text{tl}_1 \ \text{rl}_1 \ r_1) \rceil$ 
  THEN1 PC_T1 "prop_eq" asm_prove_tac);
a(all_asm_fc_tac);
val TriOp_OKd_lemma = save_pop_thm "TriOp_OKd_lemma";

```

## 6.8 Conversions

Omitted. see [4].

## 6.9 Sterling

Omitted. see [4].

## 6.10 Dinary

Omitted. see [4].

## 6.11 Declarations

Omitted. see [4].

## 6.12 Case Expressions

The proof below could probably be reworked to run rather quicker by taking care to move the lemmas proved by *tac1* ... *tac4* before the case split.

SML

```

set_goal([],  $\ulcorner \forall c \text{ te cel ee} \bullet$ 
  te  $\in$  OK_VC_d c  $\wedge$ 
  Elems (Map Fst cel)  $\subseteq$  OK_VC_d c  $\wedge$ 
  Elems (Map Snd cel)  $\subseteq$  OK_VC_d c  $\wedge$ 
  ee  $\in$  OK_VC_d c  $\Rightarrow$ 
  CaseVal c te cel ee  $\in$  OK_VC_d c
 $\urcorner$ );
a(REPEAT  $\forall$ _tac);
a(list_induction_tac $\urcorner$  cel $\urcorner$  THEN REPEAT strip_tac);
(* *** Goal "1" *** *)
a(rewrite_tac(CaseVal_lemma:: map get_spec[ $\urcorner$  OK_VC_d $\urcorner$ ,  $\urcorner$  Let $\urcorner$ ])
  THEN REPEAT  $\forall$ _tac);
a(rewrite_tac[ $\forall$ _elim $\urcorner$  Snd $\urcorner$  fun_if_thm]);
a(CASES_T $\urcorner$  c dominates Fst (te tl0 rl0 r0) $\urcorner$  rewrite_thm_tac);
a(CASES_T $\urcorner$  c dominates Fst (te tl1 rl1 r1) $\urcorner$  rewrite_thm_tac);
(* *** Goal "1.1" (duplicates "1.2") *** *)
a(REPEAT strip_tac THEN
  DROP_ASM_T  $\urcorner$  ee  $\in$  OK_VC_d c $\urcorner$ 
  (fn th  $\Rightarrow$  all_fc_tac[rewrite_rule(map get_spec[ $\urcorner$  OK_VC_d $\urcorner$ ])th])
  THEN REPEAT strip_tac);
(*

```

SML

```

*)
(* *** Goal "2" *** *)
a(PC_T1 "sets_ext1" asm_prove_tac(map get_spec[ $\urcorner$  Map $\urcorner$ ,  $\urcorner$  Elems $\urcorner$ ]));
(* *** Goal "3" *** *)
a(PC_T1 "sets_ext1" asm_prove_tac(map get_spec[ $\urcorner$  Map $\urcorner$ ,  $\urcorner$  Elems $\urcorner$ ]));
(* *** Goal "4" *** *)
a(rewrite_tac( $\forall$ _elim $\urcorner$  Snd $\urcorner$  fun_if_thm:: $\forall$ _elim $\urcorner$  Fst $\urcorner$  fun_if_thm::CaseVal_lemma::
  map get_spec[ $\urcorner$  OK_VC_d $\urcorner$ ,  $\urcorner$  Let $\urcorner$ ])
  THEN REPEAT  $\forall$ _tac);
a(MAP EVERY (fn t  $\Rightarrow$  CASES_T t (fn th  $\Rightarrow$  rewrite_tac[th] THEN strip_asm_tac th))
  [ $\urcorner$  Snd (te tl0 rl0 r0) = Snd (Fst x tl0 rl0 r0) $\urcorner$ ,
   $\urcorner$  c dominates Fst (te tl0 rl0 r0) $\urcorner$ ,
   $\urcorner$  c dominates Fst (Fst x tl0 rl0 r0) $\urcorner$ ]);
(*

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(*)
(* *** Goal "4.1" *** *)
val tac1 = REPEAT strip_tac THEN
  LEMMA_T  $\lceil \text{Snd } x \in \text{OK\_VC}_d \text{ } c \rceil$ 
  (fn th => all_fc_tac[rewrite_rule(map get_spec[ $\lceil \text{OK\_VC}_d \rceil$ ])th])
  THEN1 PC_T1 "sets_ext" asm_prove_tac(map get_spec[ $\lceil \text{Elems} \rceil$ ,  $\lceil \text{Map} \rceil$ ]);
val tac2 = REPEAT strip_tac THEN
  cases_tac $\lceil \text{Snd } (te \text{ } tl_0 \text{ } rl_0 \text{ } r_0) = \text{Snd } (te \text{ } tl_1 \text{ } rl_1 \text{ } r_1) \rceil$ 
  THEN_LIST [
    asm_ante_tac  $\lceil \neg \text{Snd } (te \text{ } tl_1 \text{ } rl_1 \text{ } r_1) = \text{Snd } (\text{Fst } x \text{ } tl_1 \text{ } rl_1 \text{ } r_1) \rceil$ 
    THEN POP_ASM_T (asm_rewrite_thm_tac o eq_sym_rule)
    THEN REPEAT strip_tac THEN
    LEMMA_T  $\lceil \text{Fst } x \in \text{OK\_VC}_d \text{ } c \rceil$ 
    (fn th => all_fc_tac[rewrite_rule(map get_spec[ $\lceil \text{OK\_VC}_d \rceil$ ])th])
    THEN1 PC_T1 "sets_ext" asm_prove_tac(map get_spec[ $\lceil \text{Elems} \rceil$ ,  $\lceil \text{Map} \rceil$ ])
  ],
  DROP_ASM_T  $\lceil te \in \text{OK\_VC}_d \text{ } c \rceil$ 
  (fn th => all_fc_tac[rewrite_rule(map get_spec[ $\lceil \text{OK\_VC}_d \rceil$ ])th]);
a(MAP_EVERY (fn t => CASES_T t (fn th => rewrite_tac[th] THEN strip_asm_tac th))
  [ $\lceil \text{Snd } (te \text{ } tl_1 \text{ } rl_1 \text{ } r_1) = \text{Snd } (\text{Fst } x \text{ } tl_1 \text{ } rl_1 \text{ } r_1) \rceil$ ,
   $\lceil c \text{ dominates } \text{Fst } (\text{Fst } x \text{ } tl_1 \text{ } rl_1 \text{ } r_1) \rceil$ ,
   $\lceil c \text{ dominates } \text{Fst } (te \text{ } tl_1 \text{ } rl_1 \text{ } r_1) \rceil$ ]
  THEN_LIST[tac1, tac1, tac1, tac1, tac2, tac2, tac2, tac2]);
(*)

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SML

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*)
(* *** Goal "4.2" *** *)
val tac3 = REPEAT strip_tac THEN
  cases_tac  $\lceil Snd (te tl_0 rl_0 r_0) = Snd (te tl_1 rl_1 r_1) \rceil$ 
  THEN_LIST [
    asm_ante_tac  $\lceil \neg Snd (te tl_0 rl_0 r_0) = Snd (Fst x tl_0 rl_0 r_0) \rceil$ 
    THEN asm_rewrite_tac[]
    THEN STRIP_T (asm_tac o conv_rule(RAND_C eq_sym_conv)) THEN
    LEMMA_T  $\lceil Fst x \in OK\_VC_d c \rceil$ 
      (fn th => all_fc_tac[rewrite_rule(map get_spec[ $\lceil OK\_VC_d \rceil$ ])th])
    THEN1 PC_T1 "sets_ext" asm_prove_tac(map get_spec[ $\lceil Elems \rceil$ ,  $\lceil Map \rceil$ ])
  ,
  DROP_ASM_T  $\lceil te \in OK\_VC_d c \rceil$ 
    (fn th => all_fc_tac[rewrite_rule(map get_spec[ $\lceil OK\_VC_d \rceil$ ])th]);
val tac4 = REPEAT strip_tac THEN
  DROP_ASM_T  $\lceil CaseVal c te cel ee \in OK\_VC_d c \rceil$ 
    (fn th => all_fc_tac[rewrite_rule(map get_spec[ $\lceil OK\_VC_d \rceil$ ])th]);
a(MAP_EVERY (fn t => CASES_T t (fn th => rewrite_tac[th] THEN strip_asm_tac th))
  [ $\lceil Snd (te tl_1 rl_1 r_1) = Snd (Fst x tl_1 rl_1 r_1) \rceil$ ,
    $\lceil c \text{ dominates } Fst (Fst x tl_1 rl_1 r_1) \rceil$ ,
    $\lceil c \text{ dominates } Fst (te tl_1 rl_1 r_1) \rceil$ ]
  THEN_LIST[tac3, tac3, tac3, tac3, tac4, tac4, tac4, tac4]);
val CaseVal_OK_d_lemma = save_pop_thm"CaseVal_OK_d_lemma";

```



SML

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set_goal([],  $\lceil \forall c \text{ cel } ee \bullet$ 
  Elms (Map Fst cel)  $\subseteq$  OK_VC_d c  $\wedge$ 
  Elms (Map Snd cel)  $\subseteq$  OK_VC_d c  $\wedge$ 
  ee  $\in$  OK_VC_d c  $\Rightarrow$ 
  Case c cel ee  $\in$  OK_VC_d c
 $\rceil$ );
a(rewrite_tac(map get_spec[ $\lceil$  Let  $\rceil$ ,  $\lceil$  Case  $\rceil$ ]));
a(REPEAT  $\forall$ -tac);
a(list_induction_tac $\lceil$  cel  $\rceil$  THEN REPEAT strip_tac);
(* *** Goal "1" *** *)
a(POP_ASM_T ante_tac THEN
  rewrite_tac(map get_spec[ $\lceil$  OK_VC_d  $\rceil$ ,  $\lceil$  Let  $\rceil$ ,  $\lceil$  Map  $\rceil$ ,  $\lceil$  CaseC  $\rceil$ ,  $\lceil$  CaseValue  $\rceil$ ])
  THEN REPEAT strip_tac);
(* *** Goal "2" *** *)
a(PC_T1 "sets_ext1" asm_prove_tac(map get_spec[ $\lceil$  Map  $\rceil$ ,  $\lceil$  Elms  $\rceil$ ]));
(* *** Goal "3" *** *)
a(PC_T1 "sets_ext1" asm_prove_tac(map get_spec[ $\lceil$  Map  $\rceil$ ,  $\lceil$  Elms  $\rceil$ ]));

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SML

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(* *** Goal "4" *** *)
a(DROP_NTH_ASM_T 4 ante_tac THEN
  rewrite_tac(map get_spec[ $\lceil$  Let  $\rceil$ ,  $\lceil$  Map  $\rceil$ ,  $\lceil$  CaseC  $\rceil$ ,  $\lceil$  CaseValue  $\rceil$ ,  $\lceil$  OK_VC_d  $\rceil$ ])
  THEN REPEAT strip_tac);
a(DROP_NTH_ASM_T 5 (ante_tac o list_ $\forall$ -elim
  [ $\lceil$  tl_0  $\rceil$ ,  $\lceil$  tl_1  $\rceil$ ,  $\lceil$  rl_0  $\rceil$ ,  $\lceil$  rl_1  $\rceil$ ,  $\lceil$  r_0  $\rceil$ ,  $\lceil$  r_1  $\rceil$ ])
  THEN asm_rewrite_tac[]);
a(POP_ASM_T ante_tac
  THEN cases_tac $\lceil$   $\neg$  c dominates Fst (Fst x tl_0 rl_0 r_0)  $\rceil$ 
  THEN cases_tac $\lceil$  ItemBool (Snd (Fst x tl_0 rl_0 r_0))  $\rceil$ 
  THEN cases_tac $\lceil$  ItemBool (Snd (Fst x tl_1 rl_1 r_1))  $\rceil$ 

  THEN asm_rewrite_tac[]
  THEN REPEAT strip_tac);
(* 4.1 and 4.2 the same except for asm 1 *)

```

SML

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| (* *** Goal "4.1" *** *)
| a(POP_ASM_T (fn x => id_tac));
| set_labelled_goal"4.2";
| (* *** Goal "4.2" *** *)
| a(POP_ASM_T (fn x => id_tac));
| a(lemma_tac⌈Snd x ∈ OK_VCd c⌋
|   THEN_LIST[PC_T1 "sets_ext" asm_prove_tac(map get_spec⌈Elms⌋, ⌈Map⌋),
|     all_asm_fc_tac[get_spec⌈OK_VCd⌋]];
| (* *** Goal "4.3" *** *)
| (* 4.3 and 4.4 the same except for asm 1 *)
| a(POP_ASM_T (fn x => id_tac));
| set_labelled_goal"4.4";

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SML

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| (* *** Goal "4.4" *** *)
| a(POP_ASM_T (fn x => id_tac));
| a(lemma_tac⌈Fst x ∈ OK_VCd c⌋
|   THEN1 PC_T1 "sets_ext" asm_prove_tac(map get_spec⌈Elms⌋, ⌈Map⌋));
| a(lemma_tac⌈¬ Snd (Fst x tl0 rl0 r0) = Snd (Fst x tl1 rl1 r1)⌋
|   THEN1 PC_T1 "prop_eq" asm_prove_tac[]);
| a(all_asm_fc_tac[get_spec⌈OK_VCd⌋]);
| (* *** Goal "4.5" *** *)
| a(lemma_tac⌈Fst x ∈ OK_VCd c⌋
|   THEN1 PC_T1 "sets_ext" asm_prove_tac(map get_spec⌈Elms⌋, ⌈Map⌋));
| a(lemma_tac⌈¬ Snd (Fst x tl0 rl0 r0) = Snd (Fst x tl1 rl1 r1)⌋
|   THEN1 PC_T1 "prop_eq" asm_prove_tac[]);
| a(all_asm_fc_tac[get_spec⌈OK_VCd⌋]);
| val Case_OKd_lemma = save_pop_thm"Case-OKd-lemma";

```

### 6.13 Set Functions

The main lemma (the second of the two below) is an example of strengthening an induction hypothesis almost beyond belief. The point is that, in the goal as stated, both  $rl_0$  and  $rl_1$  appear in two different ways, viz. as an argument to the “recursive” call on  $vc$  and as the operand for what we’re doing. To get a strong enough induction hypothesis we have to quantify separately over the the row lists appearing in these different roles.

SML

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| set_goal([],  $\ulcorner \forall c f vc \bullet vc \in OK\_VC_d c \Rightarrow SetFuncAll f vc \in OK\_VC_d c \urcorner$ );
| a(rewrite_tac(map get_spec[ $\ulcorner SetFuncAll \urcorner$ ,  $\ulcorner OK\_VC_d \urcorner$ ,  $\ulcorner Let \urcorner$ ])
|   THEN REPEAT strip_tac);
| a(lemma_tac $\ulcorner \neg Snd(Split(Map(vc tl_0 rl_0) rl_0)) =$ 
|   Snd(Split(Map (vc tl_1 rl_1) rl_1)) $\urcorner$ 
|   THEN1 PC-T1 "prop-eq" asm_prove_tac[]);
| a(lemma_tac  $\ulcorner \forall rl1 rl2 rl_1 \bullet$ 
|   Map (HideDerTableRow c) rl1 = Map (HideDerTableRow c) rl2  $\wedge$ 
|   Map (HideDerTableRow c) rl_0 = Map (HideDerTableRow c) rl_1
|    $\Rightarrow \neg (Snd (Split (Map (vc tl_0 rl1) rl_0))$ 
|     = (Snd (Split (Map (vc tl_1 rl2) rl_1)))
|      $\Rightarrow \neg c$  dominates lubl (Fst (Split (Map (vc tl_0 rl1) rl_0))) $\urcorner$ 
|   THEN_LIST[id_tac, all_asm_fc_tac[]];
| a(LIST_DROP_NTH_ASM_T [1, 2, 3, 4] (Combinators.K id_tac));
| a(list_induction_tac $\ulcorner rl_0 \urcorner$  THEN REPEAT strip_tac);

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SML

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| (* *** Goal "1" *** *)
| a(strip_asm_tac ( $\forall\_elim \ulcorner rl_1 \urcorner$  list_cases_thm));
| (* *** Goal "1.1" *** *)
| a(swap_asm_concl_tac
|    $\ulcorner \neg (Snd (Split (Map (vc tl_0 rl1) []))$ 
|     = (Snd (Split (Map (vc tl_1 rl2) rl_1))) $\urcorner$ 
|   THEN asm_rewrite_tac(map get_spec[ $\ulcorner Split \urcorner$ ,  $\ulcorner Map \urcorner$ ]));
| (* *** Goal "1.2" *** *)
| a(swap_asm_concl_tac
|    $\ulcorner Map (HideDerTableRow c) [] = Map (HideDerTableRow c) rl_1 \urcorner$ 
|   THEN asm_rewrite_tac(map get_spec[ $\ulcorner Map \urcorner$ ]));

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SML

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(* *** Goal "2" *** *)
a(strip_asm_tac (∀_elim⊢ rl1⊢ list_cases_thm));
(* *** Goal "2.1" *** *)
a(swap_asm_concl_tac
  ⊢ Map (HideDerTableRow c) (Cons x rl0) = Map (HideDerTableRow c) rl1⊢
  THEN asm_rewrite_tac(map_get_spec⊢ Map⊢));
(* *** Goal "2.2" *** *)
a(var_elim_nth_asm_tac 1);
a(rewrite_tac[fst_snd_split_thm, lubl_lemma,
  get_spec⊢ Map⊢, dominates_lub_lemma]);
a(swap_asm_concl_tac
  ⊢ ¬ (Snd (Split (Map (vc tl0 rl1) (Cons x rl0)))
    = (Snd (Split (Map (vc tl1 rl2) (Cons x' list2))))⊢
  THEN asm_rewrite_tac[fst_snd_split_thm, lubl_lemma,
  get_spec⊢ Map⊢, dominates_lub_lemma]);
a(GET_NTH_ASM_T 3 (strip_asm_tac o rewrite_rule[map_def]));
a(DROP_NTH_ASM_T 7 (strip_asm_tac o rewrite_rule[fst_snd_split_thm]));
a(contr_tac THEN all_asm_fc_tac[]);
val SetFuncAll_OKd_lemma = save_pop_thm"SetFuncAll_OKd_lemma";

```

SML

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set_goal([], ⊢∀c f vc • vc ∈ OK_VCd c ⇒ SetFuncDistinct f vc ∈ OK_VCd c⊢);
a(REPEAT strip_tac);
a(lemma_tac⊢ SetFuncDistinct f vc = SetFuncAll (f o Elems) vc⊢
  THEN_LIST [rewrite_tac(map_get_spec⊢ SetFuncDistinct⊢, ⊢ SetFuncAll⊢, ⊢ Let⊢),
  POP_ASM_T rewrite_thm_tac]);
a(bc_tac[SetFuncAll_OKd_lemma] THEN asm_rewrite_tac[]);
val SetFuncDistinct_OKd_lemma = save_pop_thm"SetFuncDistinct_OKd_lemma";

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SML

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set_goal([],  $\ulcorner \forall c \text{ } vc \bullet vc \in OK\_VC_d \text{ } c \wedge vc \in OK\_VC_c \text{ } c$ 
   $\Rightarrow SetFuncAllAnd \text{ } c \text{ } vc \in OK\_VC_d \text{ } c \urcorner$ );
a(rewrite_tac(map get_spec[ $\ulcorner SetFuncAllAnd \urcorner$ ,  $\ulcorner OK\_VC_d \urcorner$ ,  $\ulcorner OK\_VC_c \urcorner$ ])
  THEN REPEAT strip_tac THEN swap_nth_asm_concl_tac 1
  THEN POP_ASM_T ante_tac THEN strip_tac);
a(lemma_tac $\ulcorner$ 
  Map ( $\lambda (c', i) \bullet (c', (if \text{ } c \text{ } \textit{dominates} \text{ } c' \text{ } \textit{then} \text{ } i \text{ } \textit{else} \text{ } \textit{Arbitrary}))$ )
  (Map (vc tl0 rl0) rl0) =
  Map ( $\lambda (c', i) \bullet (c', (if \text{ } c \text{ } \textit{dominates} \text{ } c' \text{ } \textit{then} \text{ } i \text{ } \textit{else} \text{ } \textit{Arbitrary}))$ )
  (Map (vc tl1 rl1) rl1) $\urcorner$ 
  THEN_LIST [id_tac, all_fc_tac[ComputeAnd_lemma]];
a(LEMMA_T $\ulcorner$ 
   $\forall rl_0 \text{ } rl_1 \bullet$ 
  Map (HideDerTableRow c) rl0 = Map (HideDerTableRow c) rl1
   $\Rightarrow$ 
  Map ( $\lambda (c', i) \bullet (c', (if \text{ } c \text{ } \textit{dominates} \text{ } c' \text{ } \textit{then} \text{ } i \text{ } \textit{else} \text{ } \textit{Arbitrary}))$ )
  (Map (vc tl0 rl0) rl0) =
  Map ( $\lambda (c', i) \bullet (c', (if \text{ } c \text{ } \textit{dominates} \text{ } c' \text{ } \textit{then} \text{ } i \text{ } \textit{else} \text{ } \textit{Arbitrary}))$ )
  (Map (vc tl1 rl1) rl1) $\urcorner$ 
  (fn th  $\Rightarrow$  all_fc_tac[th]));
a(strip_tac);
a(list_induction_tac $\ulcorner$  rl0 $\urcorner$  THEN asm_rewrite_tac[map_null_thm, get_spec $\ulcorner$  Map $\urcorner$ ]);
(* rewrite solves base case *)
a(REPEAT strip_tac);
a(strip_asm_tac( $\forall$ _elim $\ulcorner$  rl1 $\urcorner$  list_cases_thm) THEN all_var_elim_asm_tac1);
(* *** Goal "1" *** *)
a(POP_ASM_T ante_tac THEN rewrite_tac[get_spec $\ulcorner$  Map $\urcorner$ ]);
(* *** Goal "2" *** *)
a(POP_ASM_T ante_tac THEN rewrite_tac[get_spec $\ulcorner$  Map $\urcorner$ ]);
a(strip_tac THEN ALL_ASM_FC_T rewrite_tac[]);
a(cases_tac $\ulcorner$  c dominates Fst (vc tl1 rl1 x') $\urcorner$  THEN asm_rewrite_tac[]);
a(lemma_tac $\ulcorner$  c dominates Fst (vc tl0 rl0 x) $\urcorner$ 
  THEN1 ALL_ASM_FC_T asm_rewrite_tac[]);
a(contr_tac THEN all_asm_fc_tac[]);
val SetFuncAllAnd_OKd_lemma = save_pop_thm"SetFuncAllAnd_OKd_lemma";

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SML

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set_goal([],  $\ulcorner \forall c \text{ } vc \bullet vc \in OK\_VC_d \text{ } c \wedge vc \in OK\_VC_c \text{ } c \Rightarrow SetFuncAllOr \text{ } c \text{ } vc \in OK\_VC_d \text{ } c \urcorner$ );
a(rewrite_tac(map get_spec[ $\ulcorner SetFuncAllOr \urcorner$ ,  $\ulcorner OK\_VC_d \urcorner$ ,  $\ulcorner OK\_VC_c \urcorner$ ]))
  THEN REPEAT strip_tac THEN swap_nth_asm_concl_tac 1
  THEN POP_ASM_T ante_tac THEN strip_tac);
a(lemma_tac $\ulcorner$ 
  Map ( $\lambda (c', i) \bullet (c', (if \text{ } c \text{ } \textit{dominates} \text{ } c' \text{ } \textit{then} \text{ } i \text{ } \textit{else} \text{ } Arbitrary))$ ))
  (Map (vc tl0 rl0) rl0) =
  Map ( $\lambda (c', i) \bullet (c', (if \text{ } c \text{ } \textit{dominates} \text{ } c' \text{ } \textit{then} \text{ } i \text{ } \textit{else} \text{ } Arbitrary))$ ))
  (Map (vc tl1 rl1) rl1) $\urcorner$ 
  THEN_LIST [id_tac, all_fc_tac[ComputeOr_lemma]];
a(LEMMA_T $\ulcorner$ 
   $\forall rl_0 \text{ } rl_1 \bullet$ 
  Map (HideDerTableRow c) rl0 = Map (HideDerTableRow c) rl1
   $\Rightarrow$ 
  Map ( $\lambda (c', i) \bullet (c', (if \text{ } c \text{ } \textit{dominates} \text{ } c' \text{ } \textit{then} \text{ } i \text{ } \textit{else} \text{ } Arbitrary))$ ))
  (Map (vc tl0 rl0) rl0) =
  Map ( $\lambda (c', i) \bullet (c', (if \text{ } c \text{ } \textit{dominates} \text{ } c' \text{ } \textit{then} \text{ } i \text{ } \textit{else} \text{ } Arbitrary))$ ))
  (Map (vc tl1 rl1) rl1) $\urcorner$ 
  (fn th => all_fc_tac[th]));
a(strip_tac);
a(list_induction_tac $\ulcorner$ rl0 $\urcorner$  THEN asm_rewrite_tac[map_null_thm, get_spec $\ulcorner$ Map $\urcorner$ ]);
(* rewrite solves base case *)
a(REPEAT strip_tac);
a(strip_asm_tac( $\forall\_elim \ulcorner$ rl1 $\urcorner$  list_cases_thm) THEN all_var_elim_asm_tac1);
(* *** Goal "1" *** *)
a(POP_ASM_T ante_tac THEN rewrite_tac[get_spec $\ulcorner$ Map $\urcorner$ ]);
(* *** Goal "2" *** *)
a(POP_ASM_T ante_tac THEN rewrite_tac[get_spec $\ulcorner$ Map $\urcorner$ ]);
a(strip_tac THEN ALL_ASM_FC_T rewrite_tac[]);
a(cases_tac $\ulcorner$ c dominates Fst (vc tl1 rl1 x') $\urcorner$  THEN asm_rewrite_tac[]);
a(lemma_tac $\ulcorner$ c dominates Fst (vc tl0 rl0 x') $\urcorner$ 
  THEN1 ALL_ASM_FC_T asm_rewrite_tac[]);
a(contr_tac THEN all_asm_fc_tac[]);
val SetFuncAllOr_OKd_lemma = save_pop_thm"SetFuncAllOr_OKd_lemma";

```

## 6.14 Count Functions

SML

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set_goal([],  $\lceil \forall c \text{ } vc \bullet vc \in OK\_VC_d \text{ } c \Rightarrow CountNonNull \text{ } vc \in OK\_VC_d \text{ } c \rceil$ );
a(rewrite_tac(map get_spec [ $\lceil CountNonNull \rceil$ ,  $\lceil Let \rceil$ ])
  THEN REPEAT strip_tac);
a(bc_tac[SetFuncAll_OK_d_lemma] THEN asm_rewrite_tac[]);
val CountNonNull_OK_d_lemma = save_pop_thm "CountNonNull_OK_d_lemma";

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SML

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set_goal([],  $\lceil \forall c \text{ } vc \bullet vc \in OK\_VC_d \text{ } c \Rightarrow CountDistinct \text{ } vc \in OK\_VC_d \text{ } c \rceil$ );
a(rewrite_tac(map get_spec [ $\lceil CountDistinct \rceil$ ,  $\lceil Let \rceil$ ])
  THEN REPEAT strip_tac);
a(bc_tac[SetFuncDistinct_OK_d_lemma] THEN asm_rewrite_tac[]);
val CountDistinct_OK_d_lemma = save_pop_thm "CountDistinct_OK_d_lemma";

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SML

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set_goal([],  $\lceil \forall c \text{ } vc \bullet vc \in OK\_VC_d \text{ } c \Rightarrow CommonValue \text{ } vc \in OK\_VC_d \text{ } c \rceil$ );
a(rewrite_tac(map get_spec [ $\lceil CommonValue \rceil$ ,  $\lceil Let \rceil$ ])
  THEN REPEAT strip_tac);
a(ALL_FC_T rewrite_tac [SetFuncAll_OK_d_lemma]);
val CommonValue_OK_d_lemma = save_pop_thm "CommonValue_OK_d_lemma";

```

## 6.15 ExistsTuples

SML

```

set_goal([],  $\lceil \forall c \text{ } tc \bullet tc \in OK\_TC_d \text{ } c \wedge tc \in OK\_TC_c \text{ } c \Rightarrow ExistsTuples \text{ } c \text{ } tc \in OK\_VC_d \text{ } c \rceil$ );
a(rewrite_tac(BoolItem_OneOne_lemma :: dominates_lub_lemma ::
   $\forall\_elim \lceil Fst \rceil fun\_if\_thm :: \forall\_elim \lceil Snd \rceil fun\_if\_thm ::$ 
  map get_spec [ $\lceil OK\_TC_d \rceil$ ,  $\lceil OK\_TC_c \rceil$ ,  $\lceil OK\_VC_d \rceil$ ,  $\lceil ExistsTuples \rceil$ ,  $\lceil Let \rceil$ ]));
a(REPEAT strip_tac THEN POP_ASM_T ante_tac);
a(all_asm_fc_tac[]);
a(asm_rewrite_tac[]);
a(cases_tac  $\lceil c \text{ } dominates \text{ } Fst \text{ } (tc \text{ } tl_1) \rceil$  THEN asm_rewrite_tac[BoolItem_OneOne_lemma]);
a(cases_tac  $\lceil HideDerTable \text{ } c \text{ } (Snd \text{ } (tc \text{ } tl_0)) = HideDerTable \text{ } c \text{ } (Snd \text{ } (tc \text{ } tl_1)) \rceil$ );
(* *** Goal "1" *** *)
a(POP_ASM_T ante_tac THEN rewrite_tac(MkDerTable_lemma::
  map get_spec [ $\lceil HideDerTable \rceil$ ,  $\lceil HideDerTableData \rceil$ ,  $\lceil Let \rceil$ ]));
a(strip_tac);
a(once_rewrite_tac[ $\lceil \_ \text{ } null\_map\_hide\_lemma \rceil$ ]);
a(asm_rewrite_tac[]);
(* *** Goal "2" *** *)
a(all_asm_fc_tac[]);
a(POP_ASM_T ante_tac THEN asm_rewrite_tac[]);
val ExistsTuples_OK_d_lemma = save_pop_thm "ExistsTuples_OK_d_lemma";

```

**6.16** *Single Value*

SML

```

|set_goal([],  $\lceil \forall c tc \bullet tc \in OK\_TC_d c \wedge tc \in OK\_TC_c c \Rightarrow SingleValue c tc \in OK\_VC_d c \rceil$ );
|a(rewrite_tac(BoolItem_OneOne_lemma :: dominates_lub_lemma ::
|     $\forall\_elim \lceil Fst \rceil fun\_if\_thm :: \forall\_elim \lceil Snd \rceil fun\_if\_thm ::$ 
|    map get_spec[ $\lceil OK\_TC_d \rceil$ ,  $\lceil OK\_TC_c \rceil$ ,  $\lceil OK\_VC_d \rceil$ ,  $\lceil SingleValue \rceil$ ,  $\lceil Let \rceil$ ]));
|a(REPEAT strip_tac THEN POP_ASM_T ante_tac);
|a(all_asm_fc_tac[] THEN asm_rewrite_tac[]);
|a(cases_tac  $\lceil c \text{ dominates } Fst (tc tl_1) \rceil$  THEN asm_rewrite_tac[]);
|a(cases_tac  $\lceil \neg HideDerTable c (Snd (tc tl_0)) = HideDerTable c (Snd (tc tl_1)) \rceil$ );
|(* *** Goal "1" *** *)
|a(all_asm_fc_tac[]);
|a(POP_ASM_T ante_tac THEN asm_rewrite_tac[]);

```

SML

```

|(* *** Goal "2" *** *)
|a(POP_ASM_T ante_tac THEN rewrite_tac(MkDerTable_lemma::
|    pc_rule1 "sets_ext1" prove_rule[]
|     $\lceil \{r|c \text{ dominates } DTR\_row r \wedge c \text{ dominates } DTR\_where r\} =$ 
|     $\{r|c \text{ dominates } DTR\_row r\} \cap \{r|c \text{ dominates } DTR\_where r\} \rceil$ ::
|    map get_spec[ $\lceil HideDerTable \rceil$ ,  $\lceil HideDerTableData \rceil$ ,  $\lceil Let \rceil$ ]);
|a(rewrite_tac[ $\lceil \_ \cap \_ \rceil$  lemma] THEN strip_tac);
|a(all_fc_tac[map_hide_map_hide_ $\lceil \_ \rceil$  lemma]);
|a(LEMMA_T $\lceil$ 
|    #(Map (HideDerTableRow c)
|        ((DT_rows (Snd (tc tl_0))  $\uparrow$  {r|c dominates DTR_row r})
|          $\uparrow$  {r|c dominates DTR_where r})) =
|    #(Map (HideDerTableRow c)
|        ((DT_rows (Snd (tc tl_1))  $\uparrow$  {r|c dominates DTR_row r})
|          $\uparrow$  {r|c dominates DTR_where r})) $\rceil$ 
|    (strip_asm_tac o rewrite_rule[length_map_thm])
|    THEN1 asm_rewrite_tac[]);

```



SML

```

a(cases_tac⌈
  #((DT_rows (Snd (tc tl1)) † {r|c dominates DTR_row r})
    † {r|c dominates DTR_where r}) = 1⌋
  THEN asm_rewrite_tac[]);
a(lemma_tac⌈
  #((DT_rows (Snd (tc tl0)) † {r|c dominates DTR_row r})
    † {r|c dominates DTR_where r}) = 1⌋
  THEN1 asm_rewrite_tac[]);
a(LIST_DROP_NTH_ASM_T[1,2]
  (MAP_EVERY(strip_asm_tac o rewrite_rule[length_1_thm]]));
a(asm_rewrite_tac[]);
a(lemma_tac⌈ HideDerTableRow c x = HideDerTableRow c x'⌋);
(* *** Goal "2.1" *** *)
a(DROP_NTH_ASM_T 5 ante_tac THEN asm_rewrite_tac[get_spec⌈ Map⌋]);

```

SML

```

(* *** Goal "2.2" *** *)
a(POP_ASM_T ante_tac THEN
  rewrite_tac[MkDerTableRow_lemma, let_def, get_spec⌈ HideDerTableRow⌋]);
a(strip_tac);
a(LEMMA_T⌈
  #(Map (λ (c', i) • if c dominates c'
    then (c', i)
    else (c', ValuedItemItem (MkValuedItem sterling dummyVal)))
    (DTR_cols x)) =
  #(Map (λ (c', i) • if c dominates c'
    then (c', i)
    else (c', ValuedItemItem (MkValuedItem sterling dummyVal)))
    (DTR_cols x'))⌋
  (strip_asm_tac o rewrite_rule[length_map_thm])
  THEN1 asm_rewrite_tac[]);

```

SML

```

a(asm_rewrite_tac[]);
a(cases_tacΓ# (DTR_cols x') = 1∇ THEN asm_rewrite_tac[]);
a(lemma_tacΓ# (DTR_cols x) = 1∇ THEN1 asm_rewrite_tac[]);
a(LIST_DROP_NTH_ASM_T[1,2]
  (MAP_EVERY(strip_asm_tac o rewrite_rule[length_1_thm])));
a(DROP_NTH_ASM_T 4 ante_tac THEN asm_rewrite_tac[get_specΓMap∇]);
a(cases_tacΓc dominates Fst x''∇ THEN asm_rewrite_tac[]);
a(cases_tacΓc dominates Fst x'''∇ THEN asm_rewrite_tac[]
  THEN REPEAT strip_tac THEN asm_rewrite_tac[]);
a(DROP_NTH_ASM_T 3 ante_tac THEN asm_rewrite_tac[]);
val SingleValue_OKd-lemma = save_pop_thm"SingleValue_OKd-lemma";

```

### 6.17 JoinedRowExistence

SML

```

set_goal([], Γ∀c i • JoinedRowExistence i ∈ OK_VCd c∇);
a(rewrite_tac(map get_specΓOK_VCd∇, ΓJoinedRowExistence∇)
  THEN REPEAT strip_tac);
a(GET_NTH_ASM_T 2 ante_tac THEN
  rewrite_tac(MkDerTableRow_lemma::map get_specΓHideDerTableRow∇, ΓLet∇)
  THEN REPEAT strip_tac);
a(PC_T1 "prop_eq" asm_prove_tac[]);
val JoinedRowExistence_OKd-lemma = save_pop_thm"JoinedRowExistence_OKd-lemma";

```

## 7 CLASSIFICATION OKNESS PROOFS

### 7.1 Constant Expression

SML

```

set_goal([], Γ∀c ci • DenoteConstant ci ∈ OK_VCc c∇);
a(rewrite_tac(map get_specΓOK_VCc∇, ΓDenoteConstant∇) THEN REPEAT strip_tac);
val DenoteConstant_OKc-lemma = save_pop_thm"DenoteConstant_OKc-lemma";

```

## 7.2 Contents

SML

```

| set_goal([],  $\ulcorner \forall c \ i \bullet \text{Contents } i \in \text{OK\_VC}_c \ c \urcorner$ );
| a(rewrite_tac(map get_spec[ $\ulcorner \text{OK\_VC}_c \urcorner$ ,  $\ulcorner \text{Contents} \urcorner$ ])
|   THEN REPEAT strip_tac);
| a(all_fc_tac[HideDerTableRow_Length_lemma]);
| a(cases_tac  $\ulcorner 1 \leq i \wedge i \leq \# (\text{DTR\_cols } r_1) \urcorner$  THEN asm_rewrite_tac[]);
| a(lemma_tac  $\ulcorner i \leq \# (\text{DTR\_cols } r_0) \urcorner$  THEN1 asm_rewrite_tac[]);
| a(ALL_FC_T (MAP_EVERY(ante_tac o  $\forall$ -elim $\ulcorner c \urcorner$ ) [Nth_HideDerTableRow_lemma]));
| a(asm_rewrite_tac[]);
| a(PC_T1 "prop_eq_pair" prove_tac[]);
| val Contents_OKc_lemma = save_pop_thm"Contents_OKc_lemma";

```

## 7.3 Classification

SML

```

| set_goal([],  $\ulcorner \forall c \ i \bullet \text{Classification } i \in \text{OK\_VC}_c \ c \urcorner$ );
| a(rewrite_tac(map get_spec[ $\ulcorner \text{OK\_VC}_c \urcorner$ ,  $\ulcorner \text{Classification} \urcorner$ ])
|   THEN REPEAT strip_tac);
| a(all_fc_tac[HideDerTableRow_Length_lemma]);
| a(cases_tac  $\ulcorner 1 \leq i \wedge i \leq \# (\text{DTR\_cols } r_1) \urcorner$  THEN asm_rewrite_tac[]);
| a(DROP_NTH_ASM_T 4 ante_tac THEN
|   rewrite_tac(MkDerTableRow_lemma::map get_spec[ $\ulcorner \text{Let} \urcorner$ ,  $\ulcorner \text{HideDerTableRow} \urcorner$ ])
|   THEN taut_tac);
| val Classification_OKc_lemma = save_pop_thm"Classification_OKc_lemma";

```

## 7.4 Countall

SML

```

|set_goal([],  $\ulcorner \forall c \bullet \text{CountAll} \in \text{OK\_VC}_c \urcorner$ );
|a(rewrite_tac(map get_spec[ $\ulcorner \text{OK\_VC}_c \urcorner$ ,  $\ulcorner \text{CountAll} \urcorner$ ,  $\ulcorner \text{Let} \urcorner$ ]) THEN REPEAT strip_tac);
|a(LIST_DROP_NTH_ASM_T [1,3] discard_tac);
|a(POP_ASM_T ante_tac);
|a(intro_∀_tac( $\ulcorner rl_1 \urcorner$ ,  $\ulcorner rl_1 \urcorner$ ));
|a(list_induction_tac $\ulcorner rl_0 \urcorner$ );
|(* *** Goal "1" *** *)
|a(∀_tac);
|a(strip_asm_tac (∀_elim $\ulcorner rl_1 \urcorner$  list_cases_thm)
  THEN asm_rewrite_tac(map get_spec[ $\ulcorner \text{Map} \urcorner$ ]));
|(* *** Goal "2" *** *)
|a(REPEAT ∀_tac);
|a(strip_asm_tac (∀_elim $\ulcorner rl_1 \urcorner$  list_cases_thm)
  THEN asm_rewrite_tac(map get_spec[ $\ulcorner \text{Map} \urcorner$ ]));
|a(rewrite_tac(lubl_lemma::MkDerTableRow_lemma::map get_spec[ $\ulcorner \text{Let} \urcorner$ ,  $\ulcorner \text{HideDerTableRow} \urcorner$ ])
  THEN REPEAT strip_tac);
|a(ALL_ASM_FC_T asm_rewrite_tac[]);
|val CountAll_OKc_lemma = save_pop_thm"CountAll_OKc_lemma";

```

## 7.5 Monadic

SML

```

|set_goal([],  $\ulcorner \forall c f vc \bullet vc \in \text{OK\_VC}_c \ c \Rightarrow \text{MonOp } f \ vc \in \text{OK\_VC}_c \urcorner$ );
|a(rewrite_tac(map get_spec[ $\ulcorner \text{OK\_VC}_c \urcorner$ ,  $\ulcorner \text{MonOp} \urcorner$ ,  $\ulcorner \text{Let} \urcorner$ ]) THEN REPEAT strip_tac);
|val MonOp_OKc_lemma = save_pop_thm"MonOp_OKc_lemma";

```

## 7.6 Binary

SML

```

set_goal([],
   $\lceil \forall c \text{ vcl} \bullet \text{ Elems } \text{vcl} \subseteq \text{OK\_VC}_d \text{ } c \wedge \text{ Elems } \text{vcl} \subseteq \text{OK\_VC}_c \text{ } c$ 
   $\Rightarrow \text{BinOpAnd } c \text{ vcl} \in \text{OK\_VC}_c \text{ } c \lceil$ );
a(rewrite_tac(
  map get_spec[ $\lceil \text{BinOpAnd} \lceil$ ,  $\lceil \text{OK\_VC}_d \lceil$ ,  $\lceil \text{OK\_VC}_c \lceil$ ])
  THEN REPEAT strip_tac);
a(lemma_tac $\lceil$ 
  Map ( $\lambda (c', i) \bullet (c', (\text{if } c \text{ dominates } c' \text{ then } i \text{ else Arbitrary}))$ )
  (Map ( $\lambda e \bullet e \text{ tl}_0 \text{ rl}_0 \text{ r}_0$ ) vcl) =
  Map ( $\lambda (c', i) \bullet (c', (\text{if } c \text{ dominates } c' \text{ then } i \text{ else Arbitrary}))$ )
  (Map ( $\lambda e \bullet e \text{ tl}_1 \text{ rl}_1 \text{ r}_1$ ) vcl)  $\lceil$ 
  THEN_LIST [id_tac, all_fc_tac[ComputeAnd_lemma]]);
a(asm_ante_tac  $\lceil \text{ Elems } \text{vcl} \subseteq \text{OK\_VC}_c \text{ } c \lceil$  THEN asm_ante_tac  $\lceil \text{ Elems } \text{vcl} \subseteq \text{OK\_VC}_d \text{ } c \lceil$ 
  THEN list_induction_tac $\lceil$  vcl  $\lceil$ );

```

SML

```

(* *** Goal "1" *** *)
a(asm_rewrite_tac[get_spec $\lceil$  Map  $\lceil$ ]);
(* *** Goal "2" *** *)
a(PC_T1"sets_ext1" asm_prove_tac[get_spec $\lceil$  Elems  $\lceil$ ]);
(* *** Goal "3" *** *)
a(PC_T1"sets_ext1" asm_prove_tac[get_spec $\lceil$  Elems  $\lceil$ ]);
(* *** Goal "4" *** *)
a(asm_rewrite_tac(map get_spec[ $\lceil$  Elems  $\lceil$ ,  $\lceil$  Map  $\lceil$ ]) THEN REPEAT_N 3 strip_tac);
a(LEMMA_T $\lceil$   $x \in \text{OK\_VC}_d \text{ } c \wedge x \in \text{OK\_VC}_c \text{ } c \lceil$ 
  (strip_asm_tac o rewrite_rule(map get_spec[ $\lceil \text{OK\_VC}_d \lceil$ ,  $\lceil \text{OK\_VC}_c \lceil$ ])) THEN1
  PC_T1"sets_ext1" asm_prove_tac[get_spec $\lceil$  Elems  $\lceil$ ]);
a(all_asm_fc_tac[] THEN asm_rewrite_tac[]);
a(cases_tac $\lceil$   $c \text{ dominates } \text{Fst } (x \text{ tl}_1 \text{ rl}_1 \text{ r}_1) \lceil$  THEN asm_rewrite_tac[]);
a(lemma_tac $\lceil$   $c \text{ dominates } \text{Fst } (x \text{ tl}_0 \text{ rl}_0 \text{ r}_0) \lceil$  THEN1 asm_rewrite_tac[]);
a(contr_tac THEN all_asm_fc_tac[]);
val BinOpAnd_OK_c_lemma = save_pop_thm"BinOpAnd_OK_c_lemma";

```

SML

```

| set_goal([],
|    $\lceil \forall c \text{ vcl} \bullet \text{ Elems } \text{vcl} \subseteq \text{OK\_VC}_d \text{ } c \wedge \text{ Elems } \text{vcl} \subseteq \text{OK\_VC}_c \text{ } c$ 
|    $\Rightarrow \text{BinOpOr } c \text{ vcl} \in \text{OK\_VC}_c \text{ } c \rceil$ );
| a(rewrite_tac(
|   map get_spec[ $\lceil \text{BinOpOr} \rceil$ ,  $\lceil \text{OK\_VC}_d \rceil$ ,  $\lceil \text{OK\_VC}_c \rceil$ ])
|   THEN REPEAT strip_tac);
| a(lemma_tac $\lceil$ 
|   Map ( $\lambda (c', i) \bullet (c', (\text{if } c \text{ dominates } c' \text{ then } i \text{ else Arbitrary}))$ )
|   (Map ( $\lambda e \bullet e \text{ tl}_0 \text{ rl}_0 \text{ r}_0$ ) vcl) =
|   Map ( $\lambda (c', i) \bullet (c', (\text{if } c \text{ dominates } c' \text{ then } i \text{ else Arbitrary}))$ )
|   (Map ( $\lambda e \bullet e \text{ tl}_1 \text{ rl}_1 \text{ r}_1$ ) vcl) $\rceil$ 
|   THEN_LIST [id_tac, all_fc_tac[ComputeOr_lemma]]);
| a(asm_ante_tac  $\lceil \text{ Elems } \text{vcl} \subseteq \text{OK\_VC}_c \text{ } c \rceil$  THEN asm_ante_tac  $\lceil \text{ Elems } \text{vcl} \subseteq \text{OK\_VC}_d \text{ } c \rceil$ 
|   THEN list_induction_tac $\lceil$  vcl $\rceil$ );

```

SML

```

| (* *** Goal "1" *** *)
| a(asm_rewrite_tac[get_spec $\lceil$  Map $\rceil$ ]);
| (* *** Goal "2" *** *)
| a(PC_T1"sets_ext1" asm_prove_tac[get_spec $\lceil$  Elems $\rceil$ ]);
| (* *** Goal "3" *** *)
| a(PC_T1"sets_ext1" asm_prove_tac[get_spec $\lceil$  Elems $\rceil$ ]);
| (* *** Goal "4" *** *)
| a(asm_rewrite_tac(map get_spec[ $\lceil$  Elems $\rceil$ ,  $\lceil$  Map $\rceil$ ]) THEN REPEAT_N 3 strip_tac);
| a(LEMMA_T $\lceil$   $x \in \text{OK\_VC}_d \text{ } c \wedge x \in \text{OK\_VC}_c \text{ } c \rceil$ 
|   (strip_asm_tac o rewrite_rule(map get_spec[ $\lceil \text{OK\_VC}_d \rceil$ ,  $\lceil \text{OK\_VC}_c \rceil$ ])) THEN1
|   PC_T1"sets_ext1" asm_prove_tac[get_spec $\lceil$  Elems $\rceil$ ]);
| a(all_asm_fc_tac[] THEN asm_rewrite_tac[]);
| a(cases_tac $\lceil$   $c \text{ dominates } \text{Fst } (x \text{ tl}_1 \text{ rl}_1 \text{ r}_1) \rceil$  THEN asm_rewrite_tac[]);
| a(lemma_tac $\lceil$   $c \text{ dominates } \text{Fst } (x \text{ tl}_0 \text{ rl}_0 \text{ r}_0) \rceil$  THEN1 asm_rewrite_tac[]);
| a(contr_tac THEN all_asm_fc_tac[]);
| val BinOpOr_OKc_lemma = save_pop_thm"BinOpOr_OKc_lemma";

```

SML

```

| set_goal([],  $\lceil \forall c \text{ f } \text{vc1 } \text{vc2} \bullet \text{vc1} \in \text{OK\_VC}_c \text{ } c \wedge \text{vc2} \in \text{OK\_VC}_c \text{ } c$ 
|    $\Rightarrow \text{BinOp } f \text{ vc1 } \text{vc2} \in \text{OK\_VC}_c \text{ } c \rceil$ );
| a(rewrite_tac(map get_spec[ $\lceil \text{OK\_VC}_c \rceil$ ,  $\lceil \text{BinOp} \rceil$ ,  $\lceil \text{Let} \rceil$ ])
|   THEN REPEAT strip_tac);
| a(ALL_ASM_FC_T rewrite_tac[]);
| val BinOp_OKc_lemma = save_pop_thm"BinOp_OKc_lemma";

```

## 7.7 Triadic

SML

```

| set_goal([],  $\lceil \forall c f \text{ vc1 vc2 vc3 } \bullet$ 
|    $\text{vc1} \in \text{OK\_VC}_c \text{ c} \wedge \text{vc2} \in \text{OK\_VC}_c \text{ c} \wedge \text{vc3} \in \text{OK\_VC}_c \text{ c} \Rightarrow$ 
|    $\text{TriOp } f \text{ vc1 vc2 vc3} \in \text{OK\_VC}_c \text{ c} \rceil$ );
| a(rewrite_tac(map get_spec [ $\lceil \text{OK\_VC}_c \rceil$ ,  $\lceil \text{TriOp} \rceil$ ,  $\lceil \text{Let} \rceil$ ])
|   THEN REPEAT strip_tac);
| a(ALL_ASM_FC_T rewrite_tac[]);
| val TriOp_OK_c_lemma = save_pop_thm "TriOp_OK_c_lemma";

```

## 7.8 Conversions

Omitted. see [4].

## 7.9 Sterling

Omitted. see [4].

## 7.10 Dinary

Omitted. see [4].

## 7.11 Declarations

Omitted. see [4].

## 7.12 Case Expressions

SML

```

set_goal([],  $\ulcorner \forall c \text{ te cel ee} \bullet$ 
  te  $\in$  OK_VCd c  $\wedge$ 
  te  $\in$  OK_VCc c  $\wedge$ 
  Elems (Map Fst cel)  $\subseteq$  OK_VCd c  $\wedge$ 
  Elems (Map Fst cel)  $\subseteq$  OK_VCc c  $\wedge$ 
  Elems (Map Snd cel)  $\subseteq$  OK_VCc c  $\wedge$ 
  ee  $\in$  OK_VCc c  $\Rightarrow$ 
  CaseVal c te cel ee  $\in$  OK_VCc c
 $\urcorner$ );
a(REPEAT  $\forall$ _tac);
a(list_induction_tac $\ulcorner$  cel $\urcorner$  THEN REPEAT strip_tac);
(* *** Goal "1" *** *)
a(LIST_DROP_NTH_ASM_T [1,5] (MAP_EVERY ante_tac)
  THEN rewrite_tac(CaseVal_lemma:: map get_spec $\ulcorner$  OK_VCc $\urcorner$ ,  $\ulcorner$  Let $\urcorner$ )
  THEN REPEAT strip_tac);
a(rewrite_tac $\ulcorner$   $\forall$ _elim $\urcorner$  Fst $\urcorner$  fun_if_thm]);
a(ALL_ASM_FC_T rewrite_tac[]);

```

SML

```

(* *** Goal "2" *** *)
a(PC_T1 "sets_ext1" asm_prove_tac(map get_spec $\ulcorner$  Map $\urcorner$ ,  $\ulcorner$  Elems $\urcorner$ ));
(* *** Goal "3" *** *)
a(PC_T1 "sets_ext1" asm_prove_tac(map get_spec $\ulcorner$  Map $\urcorner$ ,  $\ulcorner$  Elems $\urcorner$ ));
(* *** Goal "4" *** *)
a(PC_T1 "sets_ext1" asm_prove_tac(map get_spec $\ulcorner$  Map $\urcorner$ ,  $\ulcorner$  Elems $\urcorner$ ));
(* *** Goal "5" *** *)
a(rewrite_tac( $\forall$ _elim $\ulcorner$  Snd $\urcorner$  fun_if_thm:: $\forall$ _elim $\ulcorner$  Fst $\urcorner$  fun_if_thm::CaseVal_lemma::
  map get_spec $\ulcorner$  OK_VCc $\urcorner$ ,  $\ulcorner$  Let $\urcorner$ )
  THEN REPEAT strip_tac);
a(lemma_tac $\ulcorner$ 
  Fst (te tl0 rl0 r0) = Fst (te tl1 rl1 r1)  $\wedge$ 
  Fst (Fst x tl0 rl0 r0) = Fst (Fst x tl1 rl1 r1)  $\wedge$ 
  Fst (Snd x tl0 rl0 r0) = Fst (Snd x tl1 rl1 r1)  $\wedge$ 
  Fst (CaseVal c te cel ee tl0 rl0 r0) =
    Fst (CaseVal c te cel ee tl1 rl1 r1) $\urcorner$ );

```



SML

```

(* *** Goal "5.1" *** *)
a(lemma_tac  $\lceil$  Fst  $x \in OK\_VC_c c \wedge Snd\ x \in OK\_VC_c c \lceil$ 
  THEN1 PC_T1 "sets_ext1" asm_prove_tac(map get_spec $\lceil$  Map $\lceil$ ,  $\lceil$  Elems $\lceil$ ));
a(LIST_DROP_NTH_ASM_T [1,2,10,12]
  (MAP_EVERY(strip_asm_tac o rewrite_rule[get_spec $\lceil$  OK_Vc $\lceil$ ])));
a(ALL_ASM_FC_T rewrite_tac[]);
(* *** Goal "5.2" *** *)
a(asm_rewrite_tac[]);
a(cases_tac $\lceil$  c dominates Fst (te tl1 rl1 r1) $\lceil$ 
  THEN cases_tac $\lceil$  c dominates Fst (Fst x tl1 rl1 r1) $\lceil$ 
  THEN asm_rewrite_tac[]);
(* *** Goal "5.2.1" *** *)
a(LEMMA_T $\lceil$  Snd (te tl0 rl0 r0) = Snd (te tl1 rl1 r1)
   $\wedge$  Snd (Fst x tl0 rl0 r0) = Snd (Fst x tl1 rl1 r1) $\lceil$ 
  rewrite_thm_tac);
a(lemma_tac  $\lceil$  Fst  $x \in OK\_VC_d c \lceil$ 
  THEN1 PC_T1 "sets_ext1" asm_prove_tac(map get_spec $\lceil$  Map $\lceil$ ,  $\lceil$  Elems $\lceil$ ));
a(LIST_DROP_NTH_ASM_T [1,16]
  (MAP_EVERY(strip_asm_tac o rewrite_rule[get_spec $\lceil$  OK_Vc $\lceil$ ])));
a(lemma_tac $\lceil$  c dominates Fst (te tl0 rl0 r0)
   $\wedge$  c dominates Fst (Fst x tl0 rl0 r0) $\lceil$ 
  THEN1 asm_rewrite_tac[]);
a(contr_tac THEN all_asm_fc_tac[]);

```

SML

```

(* *** Goal "5.2.2" *** *)
a(CASES_T  $\lceil$  Snd (te tl0 rl0 r0) = Snd (Fst x tl0 rl0 r0) $\lceil$ 
  rewrite_thm_tac
  THEN CASES_T $\lceil$  Snd (te tl1 rl1 r1) = Snd (Fst x tl1 rl1 r1) $\lceil$ 
  rewrite_thm_tac);
(* *** Goal "5.2.3" *** *)
a(CASES_T  $\lceil$  Snd (te tl0 rl0 r0) = Snd (Fst x tl0 rl0 r0) $\lceil$ 
  rewrite_thm_tac
  THEN CASES_T $\lceil$  Snd (te tl1 rl1 r1) = Snd (Fst x tl1 rl1 r1) $\lceil$ 
  rewrite_thm_tac);
(* *** Goal "5.2.4" *** *)
a(CASES_T  $\lceil$  Snd (te tl0 rl0 r0) = Snd (Fst x tl0 rl0 r0) $\lceil$ 
  rewrite_thm_tac
  THEN CASES_T $\lceil$  Snd (te tl1 rl1 r1) = Snd (Fst x tl1 rl1 r1) $\lceil$ 
  rewrite_thm_tac);
val CaseVal_OK_c_lemma = save_pop_thm"CaseVal_OK_c_lemma";

```

SML

```

set_goal([],  $\ulcorner \forall c \text{ cel } ee \bullet$ 
  Elms (Map Fst cel)  $\subseteq$  OK-VCd c  $\wedge$ 
  Elms (Map Fst cel)  $\subseteq$  OK-VCc c  $\wedge$ 
  Elms (Map Snd cel)  $\subseteq$  OK-VCc c  $\wedge$ 
  ee  $\in$  OK-VCc c  $\Rightarrow$ 
  Case c cel ee  $\in$  OK-VCc c
 $\urcorner$ );
a(rewrite_tac(map get_spec[ $\ulcorner$  Let  $\urcorner$ ,  $\ulcorner$  Case  $\urcorner$ ]));
a(REPEAT  $\forall$ -tac);
a(list_induction_tac $\ulcorner$  cel  $\urcorner$  THEN REPEAT strip_tac);
(* *** Goal "1" *** *)
a(POP_ASM_T ante_tac THEN
  rewrite_tac(map get_spec[ $\ulcorner$  OK-VCc  $\urcorner$ ,  $\ulcorner$  Let  $\urcorner$ ,  $\ulcorner$  Map  $\urcorner$ ,  $\ulcorner$  CaseC  $\urcorner$ ,  $\ulcorner$  CaseValue  $\urcorner$ ])
  THEN REPEAT strip_tac);
(* *** Goal "2" *** *)
a(PC_T1 "sets_ext1" asm_prove_tac(map get_spec[ $\ulcorner$  Map  $\urcorner$ ,  $\ulcorner$  Elms  $\urcorner$ ]));
(* *** Goal "3" *** *)
a(PC_T1 "sets_ext1" asm_prove_tac(map get_spec[ $\ulcorner$  Map  $\urcorner$ ,  $\ulcorner$  Elms  $\urcorner$ ]));
(* *** Goal "4" *** *)
a(PC_T1 "sets_ext1" asm_prove_tac(map get_spec[ $\ulcorner$  Map  $\urcorner$ ,  $\ulcorner$  Elms  $\urcorner$ ]));

```

SML

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(* *** Goal "5" *** *)
a(rewrite_tac(map get_spec[ $\ulcorner$ Let $\urcorner$ , $\ulcorner$ Map $\urcorner$ , $\ulcorner$ CaseC $\urcorner$ , $\ulcorner$ CaseValue $\urcorner$ , $\ulcorner$ OK_VC $\urcorner$ ]);
  THEN REPEAT strip_tac);
a(lemma_tac $\ulcorner$ Fst x  $\in$  OK_VC $\urcorner$  c $\urcorner$  THEN1
  PC_T1 "sets_ext1" asm_prove_tac(map get_spec[ $\ulcorner$ Map $\urcorner$ ,  $\ulcorner$ Elms $\urcorner$ ]));
a(POP_ASM_T (fn th => all_asm_fc_tac[rewrite_rule[get_spec $\ulcorner$ OK_VC $\urcorner$ th]]));
a(asm_rewrite_tac[]);
a(cases_tac $\ulcorner$  $\neg$  c dominates Fst (Fst x tl $_1$  rl $_1$  r $_1$ ) $\urcorner$ 
  THEN asm_rewrite_tac[]);
a(lemma_tac $\ulcorner$ Snd x  $\in$  OK_VC $\urcorner$  c $\urcorner$  THEN1
  PC_T1 "sets_ext1" asm_prove_tac(map get_spec[ $\ulcorner$ Map $\urcorner$ ,  $\ulcorner$ Elms $\urcorner$ ]));
a(POP_ASM_T (fn th => all_asm_fc_tac[rewrite_rule[get_spec $\ulcorner$ OK_VC $\urcorner$ th]]));
a(asm_rewrite_tac[]);
a(LEMMA_T $\ulcorner$ Snd (Fst x tl $_0$  rl $_0$  r $_0$ ) = Snd (Fst x tl $_1$  rl $_1$  r $_1$ ) $\urcorner$ 
  rewrite_thm_tac);
(* *** Goal "5.1" *** *)
a(lemma_tac $\ulcorner$ Fst x  $\in$  OK_VC $\urcorner$  d $\urcorner$  c $\urcorner$  THEN1
  PC_T1 "sets_ext1" asm_prove_tac(map get_spec[ $\ulcorner$ Map $\urcorner$ ,  $\ulcorner$ Elms $\urcorner$ ]));
a(POP_ASM_T (strip_asm_tac o rewrite_rule[get_spec $\ulcorner$ OK_VC $\urcorner$ d $\urcorner$ ]));
a(lemma_tac $\ulcorner$ c dominates Fst (Fst x tl $_0$  rl $_0$  r $_0$ ) $\urcorner$ 
  THEN1 asm_rewrite_tac[]);
a(contr_tac THEN all_asm_fc_tac[]);
(* *** Goal "5.2" *** *)
a(cases_tac $\ulcorner$ ItemBool (Snd (Fst x tl $_1$  rl $_1$  r $_1$ ) $\urcorner$ 
  THEN asm_rewrite_tac[]);
a(DROP_NTH_ASM_T 12 (fn th => all_asm_fc_tac[rewrite_rule[get_spec $\ulcorner$ OK_VC $\urcorner$ th]]));
val Case_OK_c_lemma = save_pop_thm"Case_OK_c_lemma";

```

### 7.13 Set Functions

SML

```

| set_goal([],  $\ulcorner \forall c f vc \bullet vc \in OK\_VC_c c \Rightarrow SetFuncAll f vc \in OK\_VC_c c \urcorner$ );
| a(rewrite_tac(map get_spec[ $\ulcorner SetFuncAll \urcorner$ ,  $\ulcorner OK\_VC_c \urcorner$ ,  $\ulcorner Let \urcorner$ ])
  THEN REPEAT strip_tac);
| a(lemma_tac  $\ulcorner \forall tl_0 tl_1 rl1 rl2 rl_1 \bullet$ 
  Map (HideDerTable c)  $tl_0 = Map (HideDerTable c) tl_1 \wedge$ 
  Map (HideDerTableRow c)  $rl1 = Map (HideDerTableRow c) rl2 \wedge$ 
  Map (HideDerTableRow c)  $rl_0 = Map (HideDerTableRow c) rl_1$ 
   $\Rightarrow Fst (Split (Map (vc tl_0 rl1) rl_0))$ 
   $= Fst(Split (Map (vc tl_1 rl2) rl_1)) \urcorner$ 
  THEN_LIST[id_tac, ALL_ASM_FC_T rewrite_tac[]]);
| a(LIST_DROP_NTH_ASM_T [1, 2, 3] discard_tac);
| a(list_induction_tac $\ulcorner rl_0 \urcorner$  THEN REPEAT strip_tac);

```

SML

```

| (* *** Goal "1" *** *)
| a(strip_asm_tac ( $\forall\_elim \ulcorner rl_1 \urcorner list\_cases\_thm$ ));
| (* *** Goal "1.1" *** *)
| a(asm_rewrite_tac(map get_spec[ $\ulcorner Map \urcorner$ ]));
| (* *** Goal "1.2" *** *)
| a(var_elim_nth_asm_tac 1);
| a(all_asm_ante_tac THEN asm_rewrite_tac(map get_spec[ $\ulcorner Map \urcorner$ ]));

```

SML

```

| (* *** Goal "2" *** *)
| a(strip_asm_tac ( $\forall\_elim \ulcorner rl_1 \urcorner list\_cases\_thm$ ));
| (* *** Goal "2.1" *** *)
| a(swap_asm_concl_tac
   $\ulcorner Map (HideDerTableRow c) (Cons x rl_0) = Map (HideDerTableRow c) rl_1 \urcorner$ 
  THEN asm_rewrite_tac(map get_spec[ $\ulcorner Map \urcorner$ ]));
| (* *** Goal "2.2" *** *)
| a(var_elim_nth_asm_tac 1);
| a(all_asm_ante_tac THEN rewrite_tac[fst_snd_split_thm, get_spec $\ulcorner Map \urcorner$ ]
  THEN REPEAT strip_tac);
| (* *** Goal "2.2.1" *** *)
| a(all_asm_fc_tac[]);
| (* *** Goal "2.2.2" *** *)
| a(all_asm_fc_tac[]);
| val SetFuncAll_OK_c_lemma = save_pop_thm"SetFuncAll_OK_c_lemma";

```

SML

```

set_goal([],  $\lceil \forall c f vc \bullet vc \in OK\_VC_c c \Rightarrow SetFuncDistinct f vc \in OK\_VC_c c \rceil$ );
a(REPEAT strip_tac);
a(lemma_tac $\lceil SetFuncDistinct f vc = SetFuncAll (f o Elems) vc \rceil$ 
  THEN_LIST [rewrite_tac(map get_spec $\lceil SetFuncDistinct \rceil$ ,  $\lceil SetFuncAll \rceil$ ,  $\lceil Let \rceil$ ),
    POP_ASM_T rewrite_thm_tac]);
a(bc_tac[SetFuncAll_OK_c_lemma] THEN asm_rewrite_tac[]);
val SetFuncDistinct_OK_c_lemma = save_pop_thm "SetFuncDistinct_OK_c_lemma";

```

SML

```

set_goal([],  $\lceil \forall c vc \bullet vc \in OK\_VC_d c \wedge vc \in OK\_VC_c c \Rightarrow SetFuncAllAnd c vc \in OK\_VC_c c \rceil$ );
a(rewrite_tac(map get_spec $\lceil SetFuncAllAnd \rceil$ ,  $\lceil OK\_VC_d \rceil$ ,  $\lceil OK\_VC_c \rceil$ ))
  THEN REPEAT strip_tac);
a(lemma_tac $\lceil$ 
  Map ( $\lambda (c', i) \bullet (c', (if c \text{ dominates } c' \text{ then } i \text{ else } Arbitrary))$ )
  (Map (vc tl0 rl0) rl0) =
  Map ( $\lambda (c', i) \bullet (c', (if c \text{ dominates } c' \text{ then } i \text{ else } Arbitrary))$ )
  (Map (vc tl1 rl1) rl1) $\rceil$ 
  THEN_LIST [id_tac, all_fc_tac[ComputeAnd_lemma]]);
a(LEMMA_T $\lceil$ 
   $\forall rl_0 rl_1 \bullet$ 
  Map (HideDerTableRow c) rl0 = Map (HideDerTableRow c) rl1
   $\Rightarrow$ 
  Map ( $\lambda (c', i) \bullet (c', (if c \text{ dominates } c' \text{ then } i \text{ else } Arbitrary))$ )
  (Map (vc tl0 rl0) rl0) =
  Map ( $\lambda (c', i) \bullet (c', (if c \text{ dominates } c' \text{ then } i \text{ else } Arbitrary))$ )
  (Map (vc tl1 rl1) rl1) $\rceil$ 
  (fn th => all_fc_tac[th]));

```

SML

```

a(strip_tac);
a(list_induction_tac⌈rl0⌋ THEN asm_rewrite_tac[map_null_thm, get_spec⌈Map⌋]];
(* rewrite solves base case *)
a(REPEAT strip_tac);
a(strip_asm_tac(∀_elim⌈rl1⌋ list_cases_thm) THEN all_var_elim_asm_tac1);
(* *** Goal "1" *** *)
a(POP_ASM_T ante_tac THEN rewrite_tac[get_spec⌈Map⌋]);
(* *** Goal "2" *** *)
a(POP_ASM_T ante_tac THEN rewrite_tac[get_spec⌈Map⌋]);
a(strip_tac THEN ALL_ASM_FC_T rewrite_tac[]);
a(cases_tac⌈c dominates Fst (vc tl1 rl1 x') THEN asm_rewrite_tac[]);
a(lemma_tac⌈c dominates Fst (vc tl0 rl0 x)
  THEN1 ALL_ASM_FC_T asm_rewrite_tac[]);
a(contr_tac THEN all_asm_fc_tac[]);
val SetFuncAllAnd_OK_c_lemma = save_pop_thm"SetFuncAllAnd_OK_c_lemma";

```

SML

```

set_goal([], ⌈∀c vc• vc ∈ OK_VCd c ∧ vc ∈ OK_VCc c ⇒ SetFuncAllOr c vc ∈ OK_VCc c⌋);
a(rewrite_tac(map get_spec[⌈SetFuncAllOr⌋, ⌈OK_VCd, ⌈OK_VCc]
  THEN REPEAT strip_tac);
a(lemma_tac⌈
  Map (λ (c', i)• (c', (if c dominates c' then i else Arbitrary)))
  (Map (vc tl0 rl0) rl0) =
  Map (λ (c', i)• (c', (if c dominates c' then i else Arbitrary)))
  (Map (vc tl1 rl1) rl1)⌋
  THEN_LIST [id_tac, all_fc_tac[ComputeOr_lemma]]);
a(LEMMA_T⌈
  ∀rl0 rl1•
  Map (HideDerTableRow c) rl0 = Map (HideDerTableRow c) rl1
  ⇒
  Map (λ (c', i)• (c', (if c dominates c' then i else Arbitrary)))
  (Map (vc tl0 rl0) rl0) =
  Map (λ (c', i)• (c', (if c dominates c' then i else Arbitrary)))
  (Map (vc tl1 rl1) rl1)⌋
  (fn th => all_fc_tac[th]));

```

SML

```

a(strip_tac);
a(list_induction_tac⌈rl0⌋ THEN asm_rewrite_tac[map_null_thm, get_spec⌈Map⌋]);
(* rewrite solves base case *)
a(REPEAT strip_tac);
a(strip_asm_tac(∀_elim⌈rl1⌋ list_cases_thm) THEN all_var_elim_asm_tac1);
(* *** Goal "1" *** *)
a(POP_ASM_T ante_tac THEN rewrite_tac[get_spec⌈Map⌋]);
(* *** Goal "2" *** *)
a(POP_ASM_T ante_tac THEN rewrite_tac[get_spec⌈Map⌋]);
a(strip_tac THEN ALL_ASM_FC_T rewrite_tac[]);
a(cases_tac⌈c dominates Fst (vc tl1 rl1 x')⌋ THEN asm_rewrite_tac[]);
a(lemma_tac⌈c dominates Fst (vc tl0 rl0 x)⌋
  THEN1 ALL_ASM_FC_T asm_rewrite_tac[]);
a(contr_tac THEN all_asm_fc_tac[]);
val SetFuncAllOr_OK_c_lemma = save_pop_thm"SetFuncAllOr_OK_c_lemma";

```

## 7.14 Count Functions

SML

```

set_goal([], ⌈∀c vc•vc ∈ OK_VC_c c ⇒ CountNonNull vc ∈ OK_VC_c c⌋);
a(rewrite_tac(map get_spec⌈CountNonNull⌋, ⌈Let⌋)
  THEN REPEAT strip_tac);
a(bc_tac[SetFuncAll_OK_c_lemma] THEN asm_rewrite_tac[]);
val CountNonNull_OK_c_lemma = save_pop_thm"CountNonNull_OK_c_lemma";

```

SML

```

set_goal([], ⌈∀c vc•vc ∈ OK_VC_c c ⇒ CountDistinct vc ∈ OK_VC_c c⌋);
a(rewrite_tac(map get_spec⌈CountDistinct⌋, ⌈Let⌋)
  THEN REPEAT strip_tac);
a(bc_tac[SetFuncDistinct_OK_c_lemma] THEN asm_rewrite_tac[]);
val CountDistinct_OK_c_lemma = save_pop_thm"CountDistinct_OK_c_lemma";

```

SML

```

set_goal([], ⌈∀c vc•vc ∈ OK_VC_c c ⇒ CommonValue vc ∈ OK_VC_c c⌋);
a(rewrite_tac(map get_spec⌈CommonValue⌋, ⌈Let⌋)
  THEN REPEAT strip_tac);
a(ALL_FC_T rewrite_tac [SetFuncAll_OK_c_lemma]);
val CommonValue_OK_c_lemma = save_pop_thm"CommonValue_OK_c_lemma";

```

**7.15** *ExistsTuples*

SML

```

set_goal([],  $\lceil \forall c \bullet tc \in OK\_TC_d \ c \wedge tc \in OK\_TC_c \ c \Rightarrow ExistsTuples \ c \ tc \in OK\_VC_c \ c \rceil$ );
a(rewrite_tac(
  map get_spec [ $\lceil OK\_TC_d \rceil$ ,  $\lceil OK\_TC_c \rceil$ ,  $\lceil OK\_VC_c \rceil$ ,  $\lceil ExistsTuples \rceil$ ,  $\lceil Let \rceil$ ])
  THEN REPEAT strip_tac);
a(cases_tac  $\lceil \neg HideDerTable \ c \ (Snd \ (tc \ tl_0)) = HideDerTable \ c \ (Snd \ (tc \ tl_1)) \rceil$ );
(* *** Goal "1" *** *)
a(all_asm_fc_tac[]);
a(DROP_ASM_T  $\lceil \neg c \text{ dominates } Fst \ (tc \ tl_0) \rceil$  ante_tac THEN asm_rewrite_tac[]);
a(strip_tac THEN asm_rewrite_tac[]);

```

SML

```

(* *** Goal "2" *** *)
a(all_asm_fc_tac[]);
a(cases_tac  $\lceil c \text{ dominates } Fst \ (tc \ tl_1) \rceil$  THEN asm_rewrite_tac[]);
a(DROP_ASM_T  $\lceil HideDerTable \ c \ (Snd \ (tc \ tl_0)) = HideDerTable \ c \ (Snd \ (tc \ tl_1)) \rceil$ 
  ante_tac);
a(rewrite_tac(MkDerTable_lemma::
  pc_rule1 "sets_ext1" prove_rule[]
   $\lceil \{r \mid c \text{ dominates } DTR\_row \ r \wedge c \text{ dominates } DTR\_where \ r\} =$ 
   $\{r \mid c \text{ dominates } DTR\_row \ r\} \cap \{r \mid c \text{ dominates } DTR\_where \ r\} \rceil$ ::
   $\lceil \neg$ -lemma::
  map get_spec [ $\lceil HideDerTable \rceil$ ,  $\lceil HideDerTableData \rceil$ ,  $\lceil Let \rceil$ ])
  THEN REPEAT strip_tac);
a(all_fc_tac[map_hide_map_hide_ $\lceil \neg$ -lemma]);
a(lemma_tac  $\lceil$ 
  Map DTR_row(Map(HideDerTableRow c)
    ((DT_rows (Snd (tc tl0))  $\uparrow$  {r | c dominates DTR_row r})
       $\uparrow$  {r | c dominates DTR_where r})) =
  Map DTR_row(Map(HideDerTableRow c)
    ((DT_rows (Snd (tc tl1))  $\uparrow$  {r | c dominates DTR_row r})
       $\uparrow$  {r | c dominates DTR_where r}))  $\rceil$ 
  THEN1 asm_rewrite_tac[]);
a(POP_ASM_T ante_tac THEN
  rewrite_tac[map_o_lemma, DTR_row_o_HideDerTableRow_lemma]);
a(STRIP_T rewrite_thm_tac);
val ExistsTuples_OK_c_lemma = save_pop_thm "ExistsTuples_OK_c_lemma";

```



**7.16** *SingleValue*

SML

```

set_goal([],  $\ulcorner \forall c\ tc \bullet tc \in OK\_TC_d\ c \wedge tc \in OK\_TC_c\ c \Rightarrow SingleValue\ c\ tc \in OK\_VC_c\ c \urcorner$ );
a(rewrite_tac(map get_spec [ $\ulcorner OK\_TC_d \urcorner$ ,  $\ulcorner OK\_TC_c \urcorner$ ,  $\ulcorner OK\_VC_c \urcorner$ ,  $\ulcorner SingleValue \urcorner$ ,  $\ulcorner Let \urcorner$ ])
  THEN REPEAT strip_tac);
a(all_asm_fc_tac[] THEN asm_rewrite_tac [ $\ulcorner \forall\_elim \urcorner$   $\ulcorner Fst \urcorner$   $\ulcorner fun\_if\_thm \urcorner$ ]);
a(cases_tac  $\ulcorner c\ dominates\ Fst\ (tc\ tl_1) \urcorner$  THEN asm_rewrite_tac[]);
a(lemma_tac  $\ulcorner c\ dominates\ Fst\ (tc\ tl_0) \urcorner$  THEN1 asm_rewrite_tac[]);
a(cases_tac  $\ulcorner \neg HideDerTable\ c\ (Snd\ (tc\ tl_0)) = HideDerTable\ c\ (Snd\ (tc\ tl_1)) \urcorner$ 
  THEN1 all_asm_fc_tac[]);
a(POP_ASM_T ante_tac THEN rewrite_tac (MkDerTable_lemma::
  pc_rule1 "sets_ext1" prove_rule[]
   $\ulcorner \{r|c\ dominates\ DTR\_row\ r\} \wedge c\ dominates\ DTR\_where\ r\} =$ 
   $\{r|c\ dominates\ DTR\_row\ r\} \cap \{r|c\ dominates\ DTR\_where\ r\} \urcorner$ ::
  map get_spec [ $\ulcorner HideDerTable \urcorner$ ,  $\ulcorner HideDerTableData \urcorner$ ,  $\ulcorner Let \urcorner$ ]));
a(rewrite_tac [ $\ulcorner \_ \cap \_ lemma \urcorner$ ] THEN strip_tac);
a(all_fc_tac [map_hide_map_hide  $\_ \_ lemma$ ]);

```

SML

```

a(LEMMA_T  $\ulcorner$ 
  #(Map (HideDerTableRow c)
    ((DT_rows (Snd (tc tl_0))  $\uparrow$   $\{r|c\ dominates\ DTR\_row\ r\}$ )
       $\uparrow$   $\{r|c\ dominates\ DTR\_where\ r\}$ )) =
  #(Map (HideDerTableRow c)
    ((DT_rows (Snd (tc tl_1))  $\uparrow$   $\{r|c\ dominates\ DTR\_row\ r\}$ )
       $\uparrow$   $\{r|c\ dominates\ DTR\_where\ r\}$ ))  $\urcorner$ 
  (strip_asm_tac o rewrite_rule [length_map_thm])
  THEN1 asm_rewrite_tac[]);
a(cases_tac  $\ulcorner$ 
  #(((DT_rows (Snd (tc tl_1))  $\uparrow$   $\{r|c\ dominates\ DTR\_row\ r\}$ )
     $\uparrow$   $\{r|c\ dominates\ DTR\_where\ r\}$ ) = 1  $\urcorner$ 
  THEN asm_rewrite_tac[]);
a(lemma_tac  $\ulcorner$ 
  #(((DT_rows (Snd (tc tl_0))  $\uparrow$   $\{r|c\ dominates\ DTR\_row\ r\}$ )
     $\uparrow$   $\{r|c\ dominates\ DTR\_where\ r\}$ ) = 1  $\urcorner$ 
  THEN1 asm_rewrite_tac[]);
a(LIST_DROP_NTH_ASM_T [1,2]
  (MAP_EVERY (strip_asm_tac o rewrite_rule [length_1_thm])));
a(asm_rewrite_tac[]);
a(lemma_tac  $\ulcorner HideDerTableRow\ c\ x = HideDerTableRow\ c\ x' \urcorner$ );

```

SML

```

(* *** Goal "1" *** *)
a(DROP_NTH_ASM_T 5 ante_tac THEN asm_rewrite_tac[get_spec⌈Map⌋]);
(* *** Goal "2" *** *)
a(POP_ASM_T ante_tac THEN
  rewrite_tac[MkDerTableRow_lemma, let_def, get_spec⌈HideDerTableRow⌋]);
a(strip_tac);
a(LEMMA_T⌈
  #(Map (λ (c', i) • if c dominates c'
    then (c', i)
    else (c', ValuedItemItem (MkValuedItem sterling dummyVal)))
  (DTR_cols x)) =
  #(Map (λ (c', i) • if c dominates c'
    then (c', i)
    else (c', ValuedItemItem (MkValuedItem sterling dummyVal)))
  (DTR_cols x')⌋
  (strip_asm_tac o rewrite_rule[length_map_thm])
  THEN1 asm_rewrite_tac[]);

```

SML

```

a(asm_rewrite_tac[]);
a(cases_tac⌈# (DTR_cols x') = 1⌋ THEN asm_rewrite_tac[]);
a(lemma_tac⌈# (DTR_cols x) = 1⌋ THEN1 asm_rewrite_tac[]);
a(LIST_DROP_NTH_ASM_T[1,2]
  (MAP_EVERY(strip_asm_tac o rewrite_rule[length_1_thm]]));
a(DROP_NTH_ASM_T 4 ante_tac THEN asm_rewrite_tac[get_spec⌈Map⌋]);
a(cases_tac⌈c dominates Fst x''⌋ THEN asm_rewrite_tac[]);
(* *** Goal "2.1" *** *)
a(cases_tac⌈c dominates Fst x'''⌋ THEN asm_rewrite_tac[]
  THEN REPEAT strip_tac THEN asm_rewrite_tac[]);
(* *** Goal "2.2" *** *)
a(cases_tac⌈c dominates Fst x'''⌋ THEN asm_rewrite_tac[]);
a(PC_T1 "prop_eq_pair" prove_tac[]);
val SingleValue_OKc_lemma = save_pop_thm"SingleValue_OKc_lemma";

```

### 7.17 JoinedRowExistence

SML

```

set_goal([], ⌈∀c i • JoinedRowExistence i ∈ OK_VCc c⌋);
a(rewrite_tac(map get_spec⌈OK_VCc⌋, ⌈JoinedRowExistence⌋)
  THEN REPEAT strip_tac);
val JoinedRowExistence_OKc_lemma = save_pop_thm"JoinedRowExistence_OKc_lemma";

```

## 8 CLOSING DOWN

## 9 THE THEORY fef033

### 9.1 Parents

*fef031 fef032*

### 9.2 Children

*fef035*

### 9.3 Theorems

**TableComputations\_consistent**

**ValueComputations\_consistent**

$\vdash$  *Consistent*

$(\lambda (TableComputations', ValueComputations')$

•  $\forall cc$

•  $(TableComputations' cc,$   
 $ValueComputations' cc)$

$= \bigcap_2$

$\{(tes, es)$

$|\ (\forall ci \bullet DenoteConstant\ ci \in es)$

$\wedge (\forall i \bullet Contents\ i \in es)$

$\wedge (\forall i \bullet Classification\ i \in es)$

$\wedge CountAll \in es$

$\wedge (\forall f\ e \bullet e \in es \Rightarrow MonOp\ f\ e \in es)$

$\wedge (\forall f\ e1\ e2$

•  $e1 \in es \wedge e2 \in es$

$\Rightarrow BinOp\ f\ e1\ e2 \in es)$

$\wedge (\forall f\ e1\ e2\ e3$

•  $e1 \in es \wedge e2 \in es \wedge e3 \in es$

$\Rightarrow TriOp\ f\ e1\ e2\ e3 \in es)$

$\wedge (\forall el$

•  $Elms\ el \subseteq es$

$\Rightarrow BinOpAnd\ cc\ el \in es)$

$\wedge (\forall el$

•  $Elms\ el \subseteq es$

$\Rightarrow BinOpOr\ cc\ el \in es)$

$\wedge (\forall te\ cel\ ee$

•  $te \in es$

$\wedge Elms\ (Map\ Fst\ cel) \subseteq es$

$\wedge Elms\ (Map\ Snd\ cel) \subseteq es$

$\wedge ee \in es$

$\Rightarrow CaseVal\ cc\ te\ cel\ ee \in es)$

$\wedge (\forall cel\ ee$

•  $Elms\ (Map\ Fst\ cel) \subseteq es$

$\wedge Elms\ (Map\ Snd\ cel) \subseteq es$

$$\begin{aligned}
& \wedge ee \in es \\
& \Rightarrow \text{Case } cc \text{ cel } ee \in es) \\
& \wedge (\forall e \\
& \bullet e \in es \Rightarrow \text{SetFuncAllAnd } cc \text{ } e \in es) \\
& \wedge (\forall e \\
& \bullet e \in es \Rightarrow \text{SetFuncAllOr } cc \text{ } e \in es) \\
& \wedge (\forall e \\
& \bullet e \in es \Rightarrow \text{CountNonNull } e \in es) \\
& \wedge (\forall e \\
& \bullet e \in es \Rightarrow \text{CountDistinct } e \in es) \\
& \wedge (\forall e \\
& \bullet e \in es \Rightarrow \text{CommonValue } e \in es) \\
& \wedge (\forall f \text{ } e \\
& \bullet e \in es \Rightarrow \text{SetFuncAll } f \text{ } e \in es) \\
& \wedge (\forall f \text{ } e \\
& \bullet e \in es \\
& \Rightarrow \text{SetFuncDistinct } f \text{ } e \in es) \\
& \wedge (\forall te \\
& \bullet te \in tes \\
& \Rightarrow \text{ExistsTuples } cc \text{ } te \in es) \\
& \wedge (\forall te \\
& \bullet te \in tes \\
& \Rightarrow \text{SingleValue } cc \text{ } te \in es) \\
& \wedge \text{JoinedRowExistence } cc \in es \\
& \wedge (\forall i \bullet \text{TableContents } i \in tes) \\
& \wedge (\forall esl \text{ } tel \text{ } e1 \text{ } ml \text{ } nl \text{ } e2 \\
& \bullet \text{Elems } (\text{Map } \text{Fst } esl) \subseteq es \\
& \quad \wedge \text{Elems } tel \subseteq tes \\
& \quad \wedge e1 \in es \\
& \quad \wedge e2 \in es \\
& \Rightarrow \text{AllTuples} \\
& \quad cc \\
& \quad esl \\
& \quad tel \\
& \quad e1 \\
& \quad ml \\
& \quad nl \\
& \quad e2 \\
& \in tes))
\end{aligned}$$
**BoolItem\_OneOne\_lemma**

$$\vdash \forall i1 \text{ } i2 \bullet \text{BoolItem } i1 = \text{BoolItem } i2 \Leftrightarrow i1 \Leftrightarrow i2$$
**HideDerTableRow\_Length\_lemma**

$$\vdash \forall c \text{ } r1 \text{ } r2$$

- $\text{HideDerTableRow } c \text{ } r1 = \text{HideDerTableRow } c \text{ } r2$   
 $\Rightarrow \# (\text{DTR\_cols } r1) = \# (\text{DTR\_cols } r2)$

**Nth\_HideDerTableRow\_lemma**

$$\vdash \forall c \text{ } r \text{ } i$$

- $1 \leq i \wedge i \leq \# (\text{DTR\_cols } r)$

$$\begin{aligned} &\Rightarrow \text{Nth } (DTR\_cols \ (HideDerTableRow \ c \ r)) \ i \\ &= (\text{Fst } (\text{Nth } (DTR\_cols \ r) \ i), \\ &\quad (\text{if } c \text{ dominates } \text{Fst } (\text{Nth } (DTR\_cols \ r) \ i) \\ &\quad \text{then } \text{Snd } (\text{Nth } (DTR\_cols \ r) \ i) \\ &\quad \text{else} \\ &\quad \text{ValuedItemItem} \\ &\quad (\text{MkValuedItem } \text{sterling } \text{dummyVal}))) \end{aligned}$$
**DTR\_row\_o\_HideDerTableRow\_lemma**

$$\vdash \forall c \bullet DTR\_row \ o \ HideDerTableRow \ c = DTR\_row$$
**fun\_if\_thm**

$$\vdash \forall f \ a \ b \ c$$

$$\bullet f \ (\text{if } a \ \text{then } b \ \text{else } c) = (\text{if } a \ \text{then } f \ b \ \text{else } f \ c)$$
**↑\_null\_map\_hide\_lemma**

$$\vdash \forall c \ rl$$

$$\bullet rl$$

$$\begin{aligned} &\uparrow \{r \\ &| c \text{ dominates } DTR\_row \ r \\ &\quad \wedge c \text{ dominates } DTR\_where \ r\} \\ &= [] \\ &\Leftrightarrow \text{Map} \\ &\quad (\text{HideDerTableRow } c) \\ &\quad (rl \uparrow \{r | c \text{ dominates } DTR\_row \ r\}) \\ &\quad \uparrow \{r | c \text{ dominates } DTR\_where \ r\} \\ &= [] \end{aligned}$$
**map\_hide\_map\_hide\_↑\_lemma**

$$\vdash \forall c \ rl1 \ rl2$$

$$\begin{aligned} &\bullet \text{Map } (\text{HideDerTableRow } c) \ rl1 \\ &= \text{Map } (\text{HideDerTableRow } c) \ rl2 \\ &\Rightarrow \text{Map} \\ &\quad (\text{HideDerTableRow } c) \\ &\quad (rl1 \uparrow \{r | c \text{ dominates } DTR\_where \ r\}) \\ &= \text{Map} \\ &\quad (\text{HideDerTableRow } c) \\ &\quad (rl2 \uparrow \{r | c \text{ dominates } DTR\_where \ r\}) \end{aligned}$$
**↑\_∩\_lemma**

$$\vdash \forall l \ a \ b \bullet l \uparrow (a \cap b) = l \uparrow a \uparrow b$$
**split\_thm**

$$\vdash \forall xy \ list$$

$$\begin{aligned} &\bullet \text{Split } [] = ( [], [] ) \\ &\quad \wedge \text{Split } (\text{Cons } xy \ list) \\ &\quad = (\text{Cons } (\text{Fst } xy) \ (\text{Fst } (\text{Split } list)), \\ &\quad \text{Cons } (\text{Snd } xy) \ (\text{Snd } (\text{Split } list))) \end{aligned}$$
**length\_0\_thm**

$$\vdash \forall list \bullet \# list = 0 \Leftrightarrow list = []$$
**length\_1\_thm**

$$\vdash \forall list \bullet \# list = 1 \Leftrightarrow (\exists x \bullet list = [x])$$
**fst\_snd\_split\_thm**

$$\vdash \forall list$$

$$\begin{aligned} &\bullet \text{Fst } (\text{Split } list) = \text{Map } \text{Fst } list \\ &\quad \wedge \text{Snd } (\text{Split } list) = \text{Map } \text{Snd } list \end{aligned}$$
**lubl\_lemma**

$$\vdash \forall c \ cl$$

$$\begin{aligned} &\bullet \text{lubl } [] = \text{lattice\_bottom} \\ &\quad \wedge \text{lubl } (\text{Cons } c \ cl) = c \ \text{lub} \ \text{lubl } cl \end{aligned}$$

**lub\_lattice\_bottom\_thm**

$$\vdash \forall c \bullet c \text{ lub lattice\_bottom} = c$$
**dominates\_lubl\_map\_hide\_lemma**

$$\vdash \forall cc \text{ cil}$$

- $cc \text{ dominates lubl (Map Fst cil)}$

$$\Rightarrow \text{Map}$$

$$(\lambda (c, i)$$

- $(c,$

- $(\text{if } cc \text{ dominates } c$
  - $\text{then } i$
  - $\text{else Arbitrary}))$

$$\text{cil}$$

$$= \text{cil}$$
**CaseVal\_lemma**

$$\vdash \forall cc \text{ tst cev cevs ev}$$

- $\text{CaseVal } cc \text{ tst } [] \text{ ev}$

$$= (\lambda \text{ tl rl r}$$

- $(\text{let } (tc, ti) = \text{tst } \text{tl } \text{rl } r$
- $\text{in let } (ec, ei) = \text{ev } \text{tl } \text{rl } r$
- $\text{in if } cc \text{ dominates } tc$
- $\text{then } (ec, ei)$
- $\text{else } (tc, ei))$

$$\wedge \text{CaseVal } cc \text{ tst (Cons cev cevs) ev}$$

$$= (\lambda \text{ tl rl r}$$

- $(\text{let } (cr, ir)$
- $= \text{CaseVal } cc \text{ tst cevs ev } \text{tl } \text{rl } r$
- $\text{in let } (tc, ti) = \text{tst } \text{tl } \text{rl } r$
- $\text{in let } (ce, cv) = \text{cev}$
- $\text{in let } (cec, cei) = \text{ce } \text{tl } \text{rl } r$
- $\text{in let } (cvc, cvi) = \text{cv } \text{tl } \text{rl } r$
- $\text{in if } ti = cei$
- $\text{then}$
- $\text{if}$
- $\text{cc dominates } tc$
- $\wedge \text{cc dominates } cec$
- $\text{then } (cvc, cvi)$
- $\text{else if } \neg \text{cc dominates } tc$
- $\text{then } (tc, cvi)$
- $\text{else } (cec, cvi)$
- $\text{else if}$
- $\text{cc dominates } tc$
- $\wedge \text{cc dominates } cec$
- $\text{then } (cr, ir)$
- $\text{else if } \neg \text{cc dominates } tc$
- $\text{then } (tc, ir)$
- $\text{else } (cec, ir))$

**OK\_TC<sub>d</sub>-lemma**

$$\vdash \forall c \bullet \text{OK\_TC}_d \text{ c} \subseteq \text{OkTableComputation } c$$

**DenoteConstant\_OK<sub>d</sub>-lemma**

$$\vdash \forall c \text{ ci} \bullet \text{DenoteConstant } ci \in OK\_VC_d \ c$$
**Contents\_OK<sub>d</sub>-lemma**

$$\vdash \forall c \ i \bullet \text{Contents } i \in OK\_VC_d \ c$$
**Classification\_OK<sub>d</sub>-lemma**

$$\vdash \forall c \ i \bullet \text{Classification } i \in OK\_VC_d \ c$$
**CountAll\_OK<sub>d</sub>-lemma**

$$\vdash \forall c \bullet \text{CountAll} \in OK\_VC_d \ c$$
**MonOp\_OK<sub>d</sub>-lemma**

$$\vdash \forall c \ f \ vc \bullet vc \in OK\_VC_d \ c \Rightarrow \text{MonOp } f \ vc \in OK\_VC_d \ c$$
**ComputeAnd-lemma**

$$\begin{aligned} &\vdash \forall cc \ cil1 \ cil2 \\ &\bullet \text{Map} \\ &\quad (\lambda (c, i) \\ &\quad \bullet (c, \\ &\quad \quad (\text{if } cc \text{ dominates } c \\ &\quad \quad \text{then } i \\ &\quad \quad \text{else Arbitrary}))) \\ &\quad cil1 \\ &= \text{Map} \\ &\quad (\lambda (c, i) \\ &\quad \bullet (c, \\ &\quad \quad (\text{if } cc \text{ dominates } c \\ &\quad \quad \text{then } i \\ &\quad \quad \text{else Arbitrary}))) \\ &\quad cil2 \\ &\Rightarrow \text{Fst } (\text{ComputeAnd } cc \ cil1) \\ &\quad = \text{Fst } (\text{ComputeAnd } cc \ cil2) \\ &\quad \wedge (cc \text{ dominates } \text{Fst } (\text{ComputeAnd } cc \ cil1)) \\ &\quad \Rightarrow \text{Snd } (\text{ComputeAnd } cc \ cil1) \\ &\quad = \text{Snd } (\text{ComputeAnd } cc \ cil2) \end{aligned}$$
**BinOpAnd\_OK<sub>d</sub>-lemma**

$$\begin{aligned} &\vdash \forall c \ vcl \\ &\bullet \text{Elms } vcl \subseteq OK\_VC_d \ c \wedge \text{Elms } vcl \subseteq OK\_VC_c \ c \\ &\quad \Rightarrow \text{BinOpAnd } c \ vcl \in OK\_VC_d \ c \end{aligned}$$
**ComputeOr-lemma**

$$\begin{aligned} &\vdash \forall cc \ cil1 \ cil2 \\ &\bullet \text{Map} \\ &\quad (\lambda (c, i) \\ &\quad \bullet (c, \\ &\quad \quad (\text{if } cc \text{ dominates } c \\ &\quad \quad \text{then } i \\ &\quad \quad \text{else Arbitrary}))) \\ &\quad cil1 \\ &= \text{Map} \\ &\quad (\lambda (c, i) \\ &\quad \bullet (c, \\ &\quad \quad (\text{if } cc \text{ dominates } c \end{aligned}$$



$$\begin{aligned}
& \text{then } i \\
& \text{else Arbitrary})) \\
& \text{cil2} \\
\Rightarrow & \text{Fst (ComputeOr cc cil1)} \\
& = \text{Fst (ComputeOr cc cil2)} \\
& \wedge (\text{cc dominates Fst (ComputeOr cc cil1)}) \\
\Rightarrow & \text{Snd (ComputeOr cc cil1)} \\
& = \text{Snd (ComputeOr cc cil2)}
\end{aligned}$$
**BinOpOr\_OK<sub>d</sub>-lemma**

$$\begin{aligned}
& \vdash \forall c \text{ vcl} \\
& \bullet \text{Elms vcl} \subseteq \text{OK\_VC}_d \text{ c} \wedge \text{Elms vcl} \subseteq \text{OK\_VC}_c \text{ c} \\
& \Rightarrow \text{BinOpOr c vcl} \in \text{OK\_VC}_d \text{ c}
\end{aligned}$$
**BinOp\_OK<sub>d</sub>-lemma**

$$\begin{aligned}
& \vdash \forall c \text{ f vc1 vc2} \\
& \bullet \text{vc1} \in \text{OK\_VC}_d \text{ c} \wedge \text{vc2} \in \text{OK\_VC}_d \text{ c} \\
& \Rightarrow \text{BinOp f vc1 vc2} \in \text{OK\_VC}_d \text{ c}
\end{aligned}$$
**TriOp\_OK<sub>d</sub>-lemma**

$$\begin{aligned}
& \vdash \forall c \text{ f vc1 vc2 vc3} \\
& \bullet \text{vc1} \in \text{OK\_VC}_d \text{ c} \wedge \text{vc2} \in \text{OK\_VC}_d \text{ c} \wedge \text{vc3} \in \text{OK\_VC}_d \text{ c} \\
& \Rightarrow \text{TriOp f vc1 vc2 vc3} \in \text{OK\_VC}_d \text{ c}
\end{aligned}$$
**CaseVal\_OK<sub>d</sub>-lemma**

$$\begin{aligned}
& \vdash \forall c \text{ te cel ee} \\
& \bullet \text{te} \in \text{OK\_VC}_d \text{ c} \\
& \quad \wedge \text{Elms (Map Fst cel)} \subseteq \text{OK\_VC}_d \text{ c} \\
& \quad \wedge \text{Elms (Map Snd cel)} \subseteq \text{OK\_VC}_d \text{ c} \\
& \quad \wedge \text{ee} \in \text{OK\_VC}_d \text{ c} \\
& \Rightarrow \text{CaseVal c te cel ee} \in \text{OK\_VC}_d \text{ c}
\end{aligned}$$
**Case\_OK<sub>d</sub>-lemma**

$$\begin{aligned}
& \vdash \forall c \text{ cel ee} \\
& \bullet \text{Elms (Map Fst cel)} \subseteq \text{OK\_VC}_d \text{ c} \\
& \quad \wedge \text{Elms (Map Snd cel)} \subseteq \text{OK\_VC}_d \text{ c} \\
& \quad \wedge \text{ee} \in \text{OK\_VC}_d \text{ c} \\
& \Rightarrow \text{Case c cel ee} \in \text{OK\_VC}_d \text{ c}
\end{aligned}$$
**SetFuncAll\_OK<sub>d</sub>-lemma**

$$\begin{aligned}
& \vdash \forall c \text{ f vc} \\
& \bullet \text{vc} \in \text{OK\_VC}_d \text{ c} \Rightarrow \text{SetFuncAll f vc} \in \text{OK\_VC}_d \text{ c}
\end{aligned}$$
**SetFuncDistinct\_OK<sub>d</sub>-lemma**

$$\begin{aligned}
& \vdash \forall c \text{ f vc} \\
& \bullet \text{vc} \in \text{OK\_VC}_d \text{ c} \Rightarrow \text{SetFuncDistinct f vc} \in \text{OK\_VC}_d \text{ c}
\end{aligned}$$
**SetFuncAllAnd\_OK<sub>d</sub>-lemma**

$$\begin{aligned}
& \vdash \forall c \text{ vc} \\
& \bullet \text{vc} \in \text{OK\_VC}_d \text{ c} \wedge \text{vc} \in \text{OK\_VC}_c \text{ c} \\
& \Rightarrow \text{SetFuncAllAnd c vc} \in \text{OK\_VC}_d \text{ c}
\end{aligned}$$
**SetFuncAllOr\_OK<sub>d</sub>-lemma**

$$\begin{aligned}
& \vdash \forall c \text{ vc} \\
& \bullet \text{vc} \in \text{OK\_VC}_d \text{ c} \wedge \text{vc} \in \text{OK\_VC}_c \text{ c} \\
& \Rightarrow \text{SetFuncAllOr c vc} \in \text{OK\_VC}_d \text{ c}
\end{aligned}$$
**CountNonNull\_OK<sub>d</sub>-lemma**

---

$\vdash \forall c \text{ vc} \bullet \text{vc} \in \text{OK\_VC}_d c \Rightarrow \text{CountNonNull } \text{vc} \in \text{OK\_VC}_d c$   
**CountDistinct\_OK<sub>d</sub>-lemma**

$\vdash \forall c \text{ vc} \bullet \text{vc} \in \text{OK\_VC}_d c \Rightarrow \text{CountDistinct } \text{vc} \in \text{OK\_VC}_d c$   
**CommonValue\_OK<sub>d</sub>-lemma**

$\vdash \forall c \text{ vc} \bullet \text{vc} \in \text{OK\_VC}_d c \Rightarrow \text{CommonValue } \text{vc} \in \text{OK\_VC}_d c$   
**ExistsTuples\_OK<sub>d</sub>-lemma**

$\vdash \forall c \text{ tc}$   
 $\bullet \text{tc} \in \text{OK\_TC}_d c \wedge \text{tc} \in \text{OK\_TC}_c c$   
 $\Rightarrow \text{ExistsTuples } c \text{ tc} \in \text{OK\_VC}_d c$

**SingleValue\_OK<sub>d</sub>-lemma**

$\vdash \forall c \text{ tc}$   
 $\bullet \text{tc} \in \text{OK\_TC}_d c \wedge \text{tc} \in \text{OK\_TC}_c c$   
 $\Rightarrow \text{SingleValue } c \text{ tc} \in \text{OK\_VC}_d c$

**JoinedRowExistence\_OK<sub>d</sub>-lemma**

$\vdash \forall c \text{ i} \bullet \text{JoinedRowExistence } i \in \text{OK\_VC}_d c$

**DenoteConstant\_OK<sub>c</sub>-lemma**

$\vdash \forall c \text{ ci} \bullet \text{DenoteConstant } ci \in \text{OK\_VC}_c c$

**Contents\_OK<sub>c</sub>-lemma**

$\vdash \forall c \text{ i} \bullet \text{Contents } i \in \text{OK\_VC}_c c$

**Classification\_OK<sub>c</sub>-lemma**

$\vdash \forall c \text{ i} \bullet \text{Classification } i \in \text{OK\_VC}_c c$

**CountAll\_OK<sub>c</sub>-lemma**

$\vdash \forall c \bullet \text{CountAll} \in \text{OK\_VC}_c c$

**MonOp\_OK<sub>c</sub>-lemma**

$\vdash \forall c \text{ f } \text{vc} \bullet \text{vc} \in \text{OK\_VC}_c c \Rightarrow \text{MonOp } f \text{ vc} \in \text{OK\_VC}_c c$

**BinOpAnd\_OK<sub>c</sub>-lemma**

$\vdash \forall c \text{ vcl}$   
 $\bullet \text{Elms } \text{vcl} \subseteq \text{OK\_VC}_d c \wedge \text{Elms } \text{vcl} \subseteq \text{OK\_VC}_c c$   
 $\Rightarrow \text{BinOpAnd } c \text{ vcl} \in \text{OK\_VC}_c c$

**BinOpOr\_OK<sub>c</sub>-lemma**

$\vdash \forall c \text{ vcl}$   
 $\bullet \text{Elms } \text{vcl} \subseteq \text{OK\_VC}_d c \wedge \text{Elms } \text{vcl} \subseteq \text{OK\_VC}_c c$   
 $\Rightarrow \text{BinOpOr } c \text{ vcl} \in \text{OK\_VC}_c c$

**BinOp\_OK<sub>c</sub>-lemma**

$\vdash \forall c \text{ f } \text{vc1 } \text{vc2}$   
 $\bullet \text{vc1} \in \text{OK\_VC}_c c \wedge \text{vc2} \in \text{OK\_VC}_c c$   
 $\Rightarrow \text{BinOp } f \text{ vc1 } \text{vc2} \in \text{OK\_VC}_c c$

**TriOp\_OK<sub>c</sub>-lemma**

$\vdash \forall c \text{ f } \text{vc1 } \text{vc2 } \text{vc3}$   
 $\bullet \text{vc1} \in \text{OK\_VC}_c c \wedge \text{vc2} \in \text{OK\_VC}_c c \wedge \text{vc3} \in \text{OK\_VC}_c c$   
 $\Rightarrow \text{TriOp } f \text{ vc1 } \text{vc2 } \text{vc3} \in \text{OK\_VC}_c c$

**CaseVal\_OK<sub>c</sub>-lemma**

$\vdash \forall c \text{ te } \text{cel } \text{ee}$   
 $\bullet \text{te} \in \text{OK\_VC}_d c$   
 $\wedge \text{te} \in \text{OK\_VC}_c c$   
 $\wedge \text{Elms } (\text{Map } \text{Fst } \text{cel}) \subseteq \text{OK\_VC}_d c$   
 $\wedge \text{Elms } (\text{Map } \text{Fst } \text{cel}) \subseteq \text{OK\_VC}_c c$   
 $\wedge \text{Elms } (\text{Map } \text{Snd } \text{cel}) \subseteq \text{OK\_VC}_c c$

---

$$\begin{aligned} & \wedge ee \in OK\_VC_c c \\ & \Rightarrow CaseVal c te cel ee \in OK\_VC_c c \end{aligned}$$

**Case\_OK<sub>c</sub>-lemma**

$$\begin{aligned} & \vdash \forall c cel ee \\ & \bullet Elems (Map Fst cel) \subseteq OK\_VC_d c \\ & \quad \wedge Elems (Map Fst cel) \subseteq OK\_VC_c c \\ & \quad \wedge Elems (Map Snd cel) \subseteq OK\_VC_c c \\ & \quad \wedge ee \in OK\_VC_c c \\ & \Rightarrow Case c cel ee \in OK\_VC_c c \end{aligned}$$

**SetFuncAll\_OK<sub>c</sub>-lemma**

$$\begin{aligned} & \vdash \forall c f vc \\ & \bullet vc \in OK\_VC_c c \Rightarrow SetFuncAll f vc \in OK\_VC_c c \end{aligned}$$

**SetFuncDistinct\_OK<sub>c</sub>-lemma**

$$\begin{aligned} & \vdash \forall c f vc \\ & \bullet vc \in OK\_VC_c c \Rightarrow SetFuncDistinct f vc \in OK\_VC_c c \end{aligned}$$

**SetFuncAllAnd\_OK<sub>c</sub>-lemma**

$$\begin{aligned} & \vdash \forall c vc \\ & \bullet vc \in OK\_VC_d c \wedge vc \in OK\_VC_c c \\ & \quad \Rightarrow SetFuncAllAnd c vc \in OK\_VC_c c \end{aligned}$$

**SetFuncAllOr\_OK<sub>c</sub>-lemma**

$$\begin{aligned} & \vdash \forall c vc \\ & \bullet vc \in OK\_VC_d c \wedge vc \in OK\_VC_c c \\ & \quad \Rightarrow SetFuncAllOr c vc \in OK\_VC_c c \end{aligned}$$

**CountNonNull\_OK<sub>c</sub>-lemma**

$$\vdash \forall c vc \bullet vc \in OK\_VC_c c \Rightarrow CountNonNull vc \in OK\_VC_c c$$

**CountDistinct\_OK<sub>c</sub>-lemma**

$$\vdash \forall c vc \bullet vc \in OK\_VC_c c \Rightarrow CountDistinct vc \in OK\_VC_c c$$

**CommonValue\_OK<sub>c</sub>-lemma**

$$\vdash \forall c vc \bullet vc \in OK\_VC_c c \Rightarrow CommonValue vc \in OK\_VC_c c$$

**ExistsTuples\_OK<sub>c</sub>-lemma**

$$\begin{aligned} & \vdash \forall c tc \\ & \bullet tc \in OK\_TC_d c \wedge tc \in OK\_TC_c c \\ & \quad \Rightarrow ExistsTuples c tc \in OK\_VC_c c \end{aligned}$$

**SingleValue\_OK<sub>c</sub>-lemma**

$$\begin{aligned} & \vdash \forall c tc \\ & \bullet tc \in OK\_TC_d c \wedge tc \in OK\_TC_c c \\ & \quad \Rightarrow SingleValue c tc \in OK\_VC_c c \end{aligned}$$

**JoinedRowExistence\_OK<sub>c</sub>-lemma**

$$\vdash \forall c i \bullet JoinedRowExistence i \in OK\_VC_c c$$

## 10 INDEX

<i>BinOpAnd_OK_c_lemma</i> .....	37	<i>SetFuncAll_OK_c_lemma</i> .....	44
<i>BinOpAnd_OK_d_lemma</i> .....	17	<i>SetFuncAll_OK_d_lemma</i> .....	28
<i>BinOpOr_OK_c_lemma</i> .....	38	<i>SetFuncDistinct_OK_c_lemma</i> .....	45
<i>BinOpOr_OK_d_lemma</i> .....	20	<i>SetFuncDistinct_OK_d_lemma</i> .....	28
<i>BinOp_OK_c_lemma</i> .....	38	<i>SingleValue_OK_c_lemma</i> .....	50
<i>BinOp_OK_d_lemma</i> .....	21	<i>SingleValue_OK_d_lemma</i> .....	34
<i>BoolItem_OneOne_lemma</i> .....	5	<i>split_thm</i> .....	8
<i>CaseVal_lemma</i> .....	11	<i>TriOp_OK_c_lemma</i> .....	39
<i>CaseVal_OK_c_lemma</i> .....	41	<i>TriOp_OK_d_lemma</i> .....	21
<i>CaseVal_OK_d_lemma</i> .....	24	$\vdash\_null\_map\_hide\_lemma$ .....	7
<i>Case_OK_c_lemma</i> .....	43	$\vdash\_ \cap\_lemma$ .....	8
<i>Case_OK_d_lemma</i> .....	26		
<i>Classification_OK_c_lemma</i> .....	35		
<i>Classification_OK_d_lemma</i> .....	13		
<i>CommonValue_OK_c_lemma</i> .....	47		
<i>CommonValue_OK_d_lemma</i> .....	31		
<i>ComputeAnd_lemma</i> .....	16		
<i>ComputeOr_lemma</i> .....	19		
<i>Contents_OK_c_lemma</i> .....	35		
<i>Contents_OK_d_lemma</i> .....	12		
<i>CountAll_OK_c_lemma</i> .....	36		
<i>CountAll_OK_d_lemma</i> .....	14		
<i>CountDistinct_OK_c_lemma</i> .....	47		
<i>CountDistinct_OK_d_lemma</i> .....	31		
<i>CountNonNull_OK_c_lemma</i> .....	47		
<i>CountNonNull_OK_d_lemma</i> .....	31		
<i>DenoteConstant_OK_c_lemma</i> .....	34		
<i>DenoteConstant_OK_d_lemma</i> .....	12		
<i>dominates_lubl_map_hide_lemma</i> .....	10		
<i>DTR_row_o_HideDerTableRow_lemma</i> .....	6		
<i>ExistsTuples_OK_c_lemma</i> .....	48		
<i>ExistsTuples_OK_d_lemma</i> .....	31		
<i>fef033</i> .....	4		
<i>fst_snd_split_thm</i> .....	9		
<i>fun_if_thm</i> .....	7		
<i>HideDerTableRow_Length_lemma</i> .....	5		
<i>JoinedRowExistence_OK_c_lemma</i> .....	50		
<i>JoinedRowExistence_OK_d_lemma</i> .....	34		
<i>length_0_thm</i> .....	8		
<i>length_1_thm</i> .....	9		
<i>lubl_lemma</i> .....	9		
<i>lub_lattice_bottom_thm</i> .....	9		
<i>map_hide_map_hide_lemma</i> .....	8		
<i>MonOp_OK_c_lemma</i> .....	36		
<i>MonOp_OK_d_lemma</i> .....	14		
<i>Nth_HideDerTableRow_lemma</i> .....	6		
<i>OK_TC_d_lemma</i> .....	12		
<i>SetFuncAllAnd_OK_c_lemma</i> .....	46		
<i>SetFuncAllAnd_OK_d_lemma</i> .....	29		
<i>SetFuncAllOr_OK_c_lemma</i> .....	47		
<i>SetFuncAllOr_OK_d_lemma</i> .....	30		